

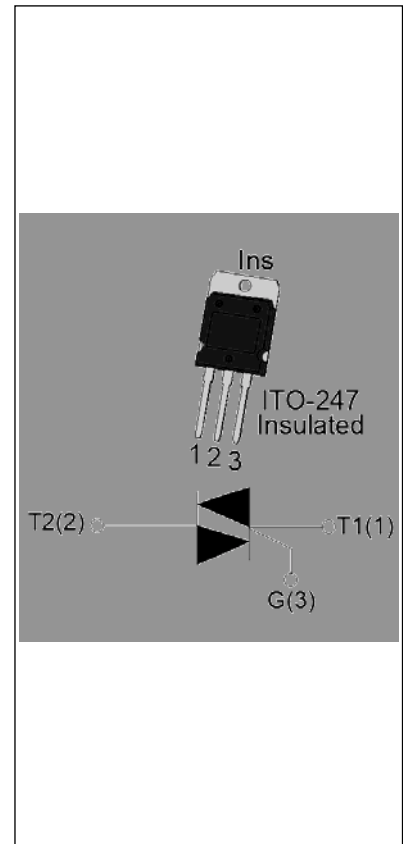


JST80IS-1200BW 80A TRIAC

Rev.A.1.0

DESCRIPTION:

The JST80IS-1200BW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. JST80IS-1200BW snubberless triac is especially recommended for use on inductive loads. By using an internal ceramic pad, JST80IS-1200BW provides a rated insulation voltage of 2500 VRMS, complying with UL standards (File ref: E252906). Package ITO-247 is RoHS compliant.



MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	80	A
V_{DRM}/V_{RRM}	1200	V
$I_{GT} / /$	50/50/50	mA

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage ($T_j=25^\circ C$)	V_{DRM}	1200	V
Repetitive peak reverse voltage ($T_j=25^\circ C$)	V_{RRM}	1200	V
RMS on-state current ($T_c = 90^\circ C$)	$I_{T(RMS)}$	80	A
Non repetitive surge peak on-state current (full cycle, $t_p=20ms$, $T_j=25^\circ C$)	I_{TSM}	800	A
Non repetitive surge peak on-state current (full cycle, $t_p=16.6ms$, $T_j=25^\circ C$)		880	
I^2t value for fusing ($t_p=10ms$, $T_j=25^\circ C$)	I^2t	3200	A^2s
Critical rate of rise of on-state current ($I_G=2 I_{GT}$, $f=100Hz$, $T_j=125^\circ C$)	di/dt	100	$A/\mu s$
Peak gate current ($t_p=20\mu s$, $T_j=125^\circ C$)	I_{GM}	10	A

Average gate power dissipation ($T_j=125$)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	25	W
Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.7)	V_{pp}	1	kV

ELECTRICAL CHARACTERISTICS($T_j=25$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V R_L=33$	- -	MAX.	50	mA
V_{GT}		- -	MAX.	1.3	V
V_{GD}	$V_D=V_{DRM} T_j=125$ $R_L=3.3K$	- -	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	-	MAX.	80	mA
				120	
I_H	$I_T=1A$		MAX.	70	mA
dV/dt	$V_D=800V$ Gate Open $T_j=125$		MIN.	2000	V/ μs
(dI/dt)c	(dV/dt)c=20V/ μs $T_j=125$		MIN.	25	A/ms
t_{on}	$I_G=80mA I_A=400mA I_R=40mA$ $T_j=25$		TYP.	8	μs
t_{off}				60	

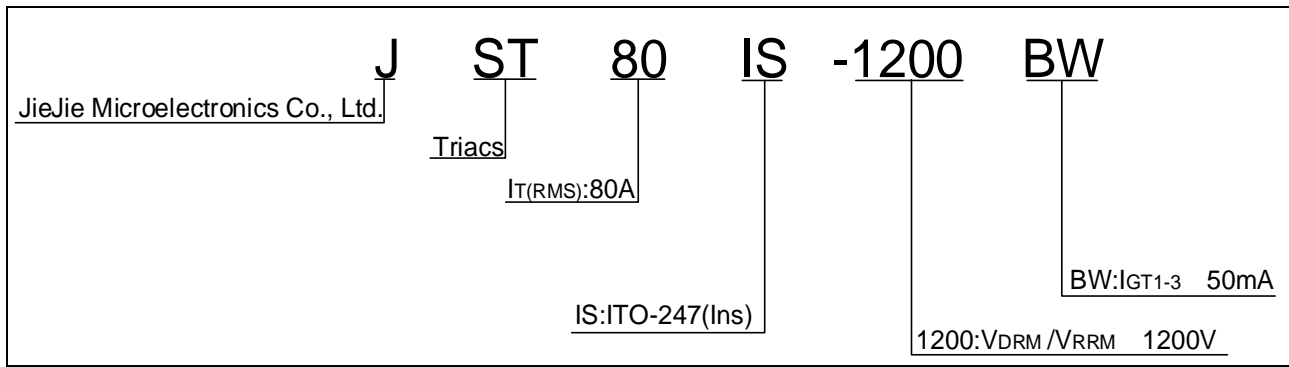
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=120A t_p=380\mu s$	$T_j=25$	1.9	V
V_{TO}	Threshold voltage	$T_j=125$	0.71	V
R_D	Dynamic resistance	$T_j=125$	23	m
I_{DRM}	$V_D=V_{DRM} V_R=V_{RRM}$	$T_j=25$	15	μA
I_{RRM}		$T_j=125$	10	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	0.27	/W
$R_{th(j-a)}$	junction to ambient (AC)	45	/W

ORDERING INFORMATION



MARKING

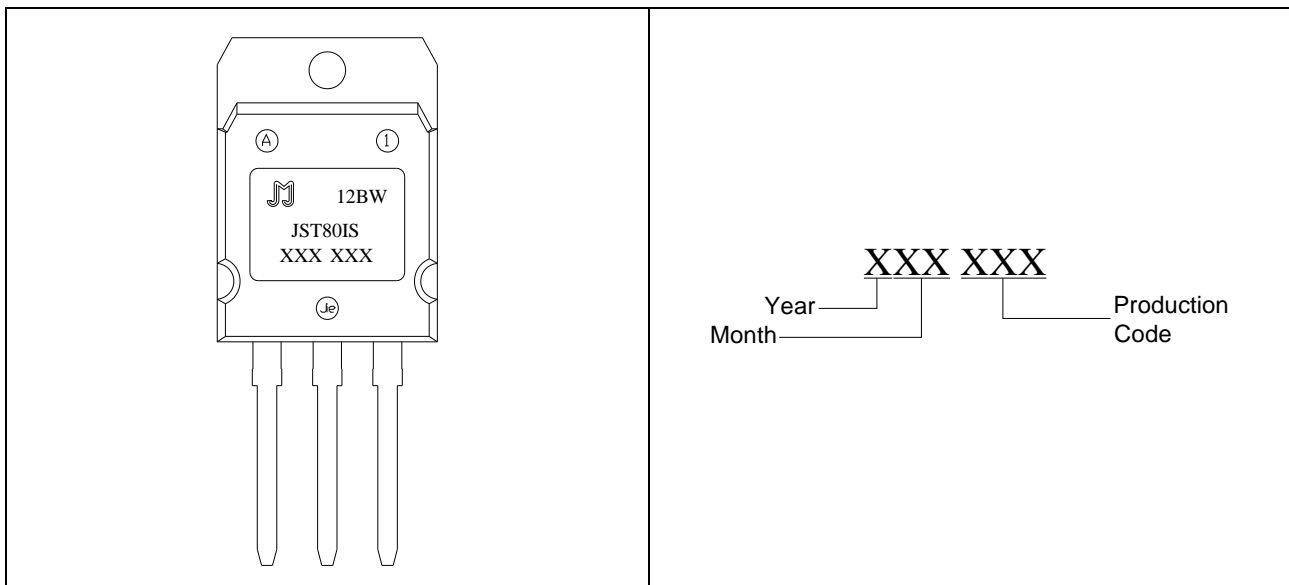


FIG.1 Maximum power dissipation versus RMS on-state current

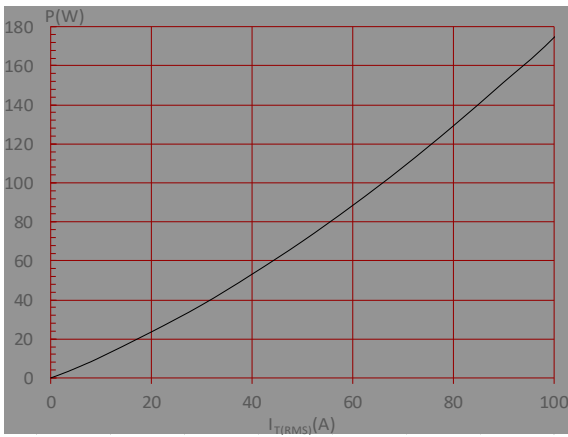


FIG.3: Surge peak on-state current versus number of cycles

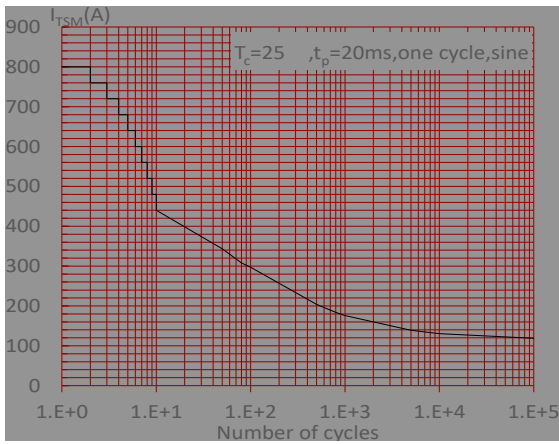


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$, and corresponding value of I^2t ($di/dt < 100\text{A}/\mu\text{s}$)

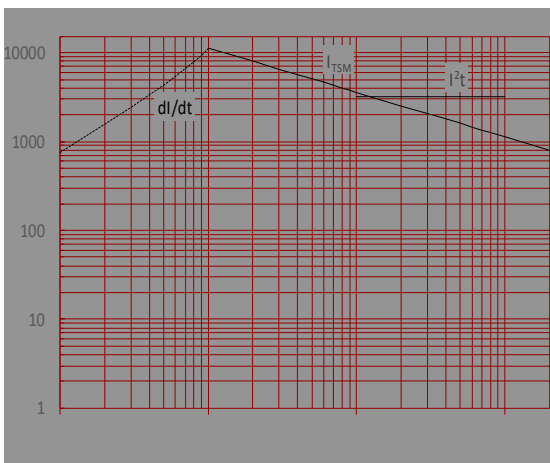


FIG.2: RMS on-state current versus case temperature

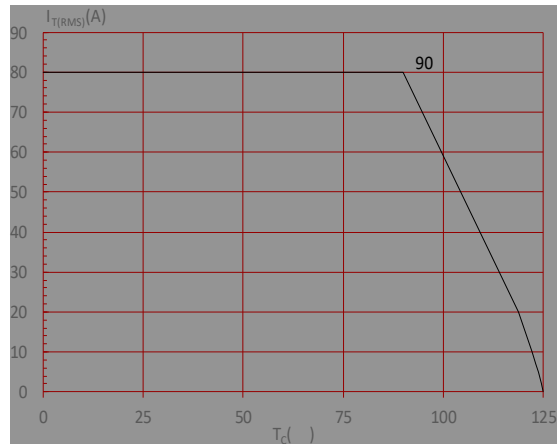


FIG.4: On-state characteristic

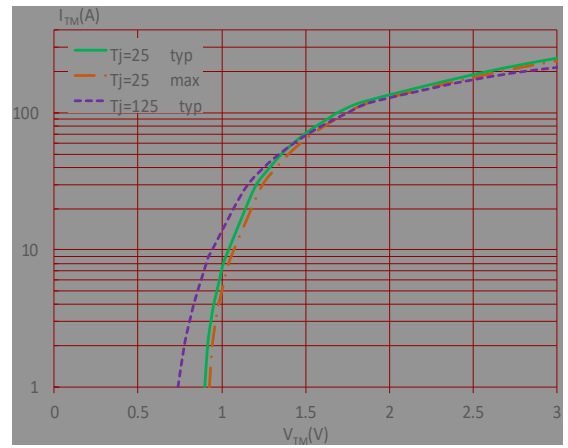
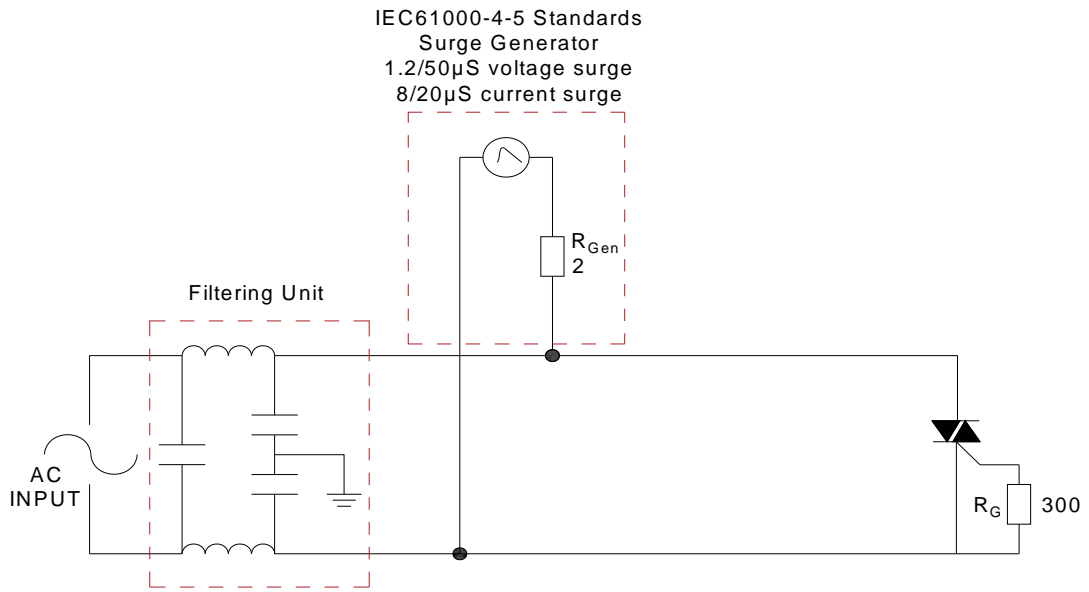


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards

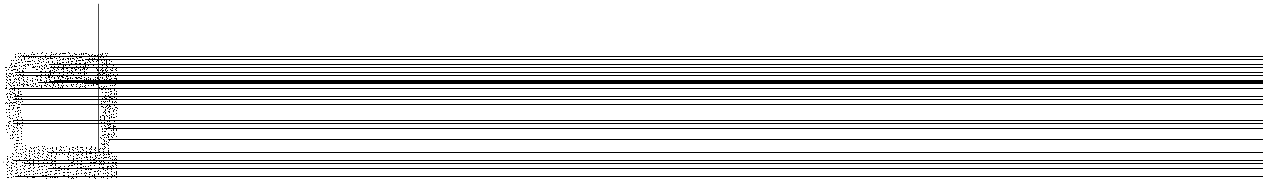


**ORDERING INFORMATION**

Order code	Voltage $V_{DRM}/V_{RRM}(V)$	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
JST80IS-1200BW	1200	50	ITO-247(Ins)	25	Tube


Document Revision History

Date	Revision	Changes
Apr.11, 2023	A.1.0	Last updated



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.

 is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.

Copyright ©2023 Jiangsu JieJie Microelectronics Co., Ltd. Printed All rights reserved.