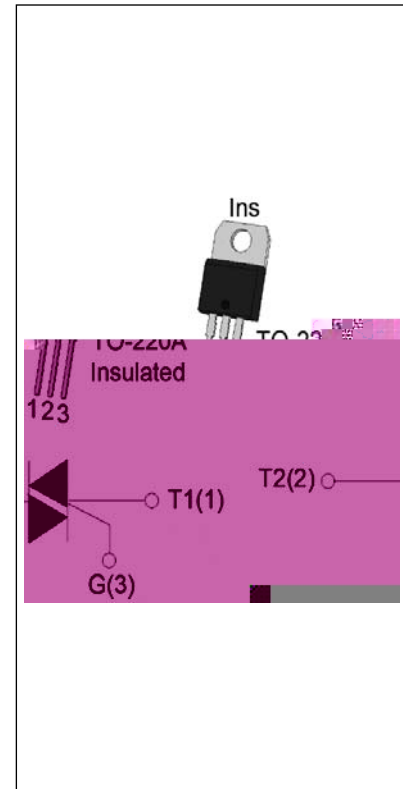


The JST24A-600CW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. JST24A-600CW snubberless triac is especially recommended for use on inductive loads. By using an internal ceramic pad, JST24A-600CW provides a rated insulation voltage of 2500 VRMS, complying with UL standards (File ref: E252906). Package TO-220A is RoHS compliant.



Symbol	Value	Unit
$I_{T(RMS)}$	25	A
$V_{DRM}/V_{RRM}$	600	V
$I_{GT\ I/II/III}$	35/35/35	mA

Storage junction temperature range	$T_{stg}$	-40-150	°C
Operating junction temperature range	$T_j$	-40-125	°C
Repetitive peak off-state voltage ( $T_j=25^\circ\text{C}$ )	$V_{DRM}$	600	V
Repetitive peak reverse voltage ( $T_j=25^\circ\text{C}$ )	$V_{RRM}$	600	V
RMS on-state current ( $T_c \leq 79^\circ\text{C}$ )	$I_{T(RMS)}$	25	A
Non repetitive surge peak on-state current (full cycle , $t_p=20\text{ms}$ , $T_j=25^\circ\text{C}$ )	$I_{TSM}$	250	A
Non repetitive surge peak on-state current (full cycle , $t_p=16.6\text{ms}$ , $T_j=25^\circ\text{C}$ )		275	
$I^2t$ value for fusing ( $t_p=10\text{ms}$ , $T_j=25^\circ\text{C}$ )	$I^2t$	340	$\text{A}^2\text{s}$
Critical rate of rise of on-state current ( $I_G=2 \times I_{GT}$ , $f=100\text{Hz}$ , $T_j=125^\circ\text{C}$ )	$di/dt$	100	$\text{A}/\mu\text{s}$
Peak gate current ( $t_p=20\mu\text{s}$ , $T_j=125^\circ\text{C}$ )	$I_{GM}$	4	A
Average gate power dissipation ( $T_j=125^\circ\text{C}$ )	$P_{G(AV)}$	0.5	W

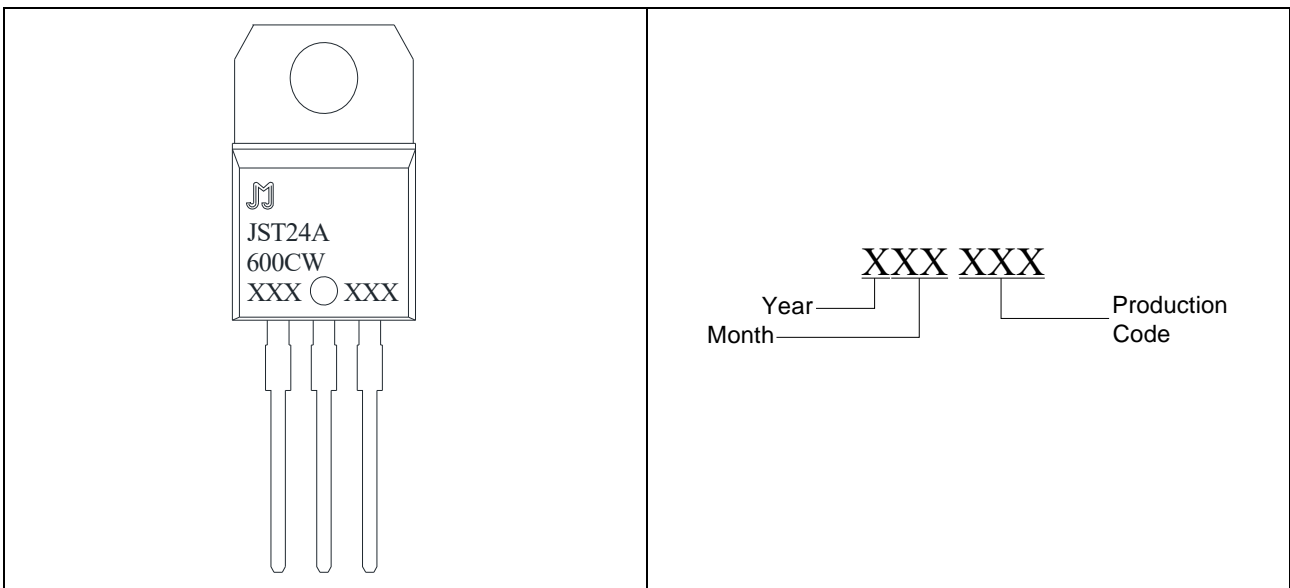
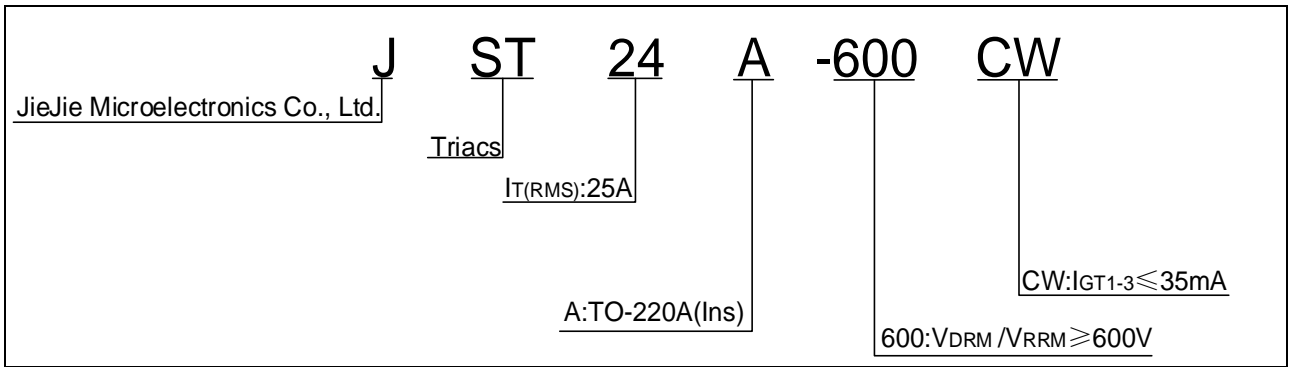


Peak gate power	$P_{GM}$	10	W
Peak pulse voltage ( $T_j=25^\circ\text{C}$ ; non-repetitive, off-state; FIG.7)	$V_{pp}$	2.5	kV

( $T_j=25^\circ\text{C}$  unless otherwise specified)

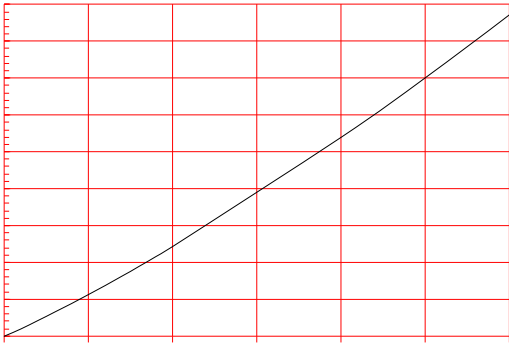
$I_{GT}$	$V_D=12\text{V } R_L=33$	I - II - III	MAX.	35	mA
$V_{GT}$		I - II - III	MAX.	1	V
$V_{GD}$	$V_D=V_{DRM} T_j=125^\circ\text{C}$ $R_L=3.3\text{K}$	I - II - III	MIN.	0.2	V
$I_L$	$I_G=1.2I_{GT}$	I - III	MAX.	70	mA
		II		80	
$I_H$	$I_T=500\text{mA}$		MAX.	50	mA
$dV/dt$	$V_D=400\text{V}$ Gate Open $T_j=125^\circ\text{C}$		MIN.	1500	V/ $\mu\text{s}$
( $dI/dt$ ) <sub>c</sub>	( $dV/dt$ ) <sub>c</sub> =20V/ $\mu\text{s}$ $T_j=125^\circ\text{C}$		MIN.	15	A/ms
$t_{on}$	$I_G=40\text{mA } I_A=200\text{mA } I_R=20\text{mA}$ $T_j=25^\circ\text{C}$		TYP.	7	$\mu\text{s}$
$t_{off}$				50	

$V_{TM}$	$I_{TM}=35\text{A } t_p=380\mu\text{s}$	$T_j=25^\circ\text{C}$	1.5	V
$V_{TO}$	Threshold voltage	$T_j=125^\circ\text{C}$	0.75	V
$R_D$	Dynamic resistance	$T_j=125^\circ\text{C}$	18	m
$I_{DRM}$	$V_D=V_{DRM} V_R=V_{RRM}$	$T_j=25^\circ\text{C}$	5	$\mu\text{A}$
$I_{RRM}$		$T_j=125^\circ\text{C}$	1.5	mA

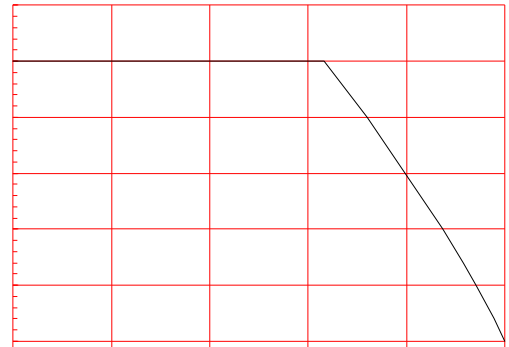




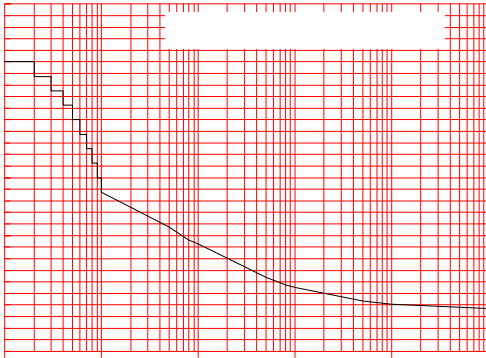
Maximum power dissipation versus RMS on-state current



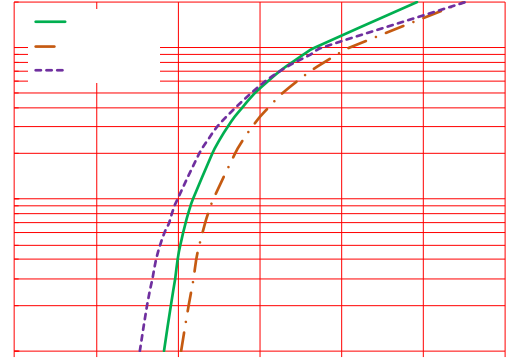
RMS on-state current versus case temperature



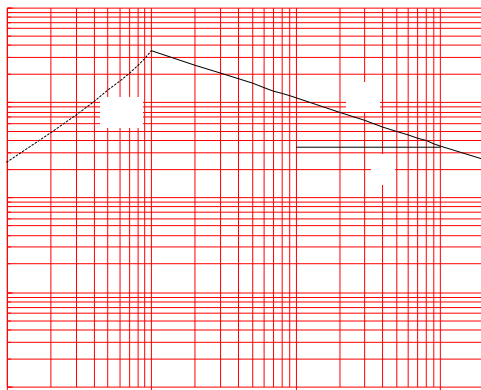
Surge peak on-state current versus number of cycles



On-state characteristics



Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 20\text{ms}$ , and corresponding value of  $I^2t$  ( $dI/dt < 100\text{A}/\mu\text{s}$ )



Relative variations of gate trigger current, holding current and latching current versus junction temperature

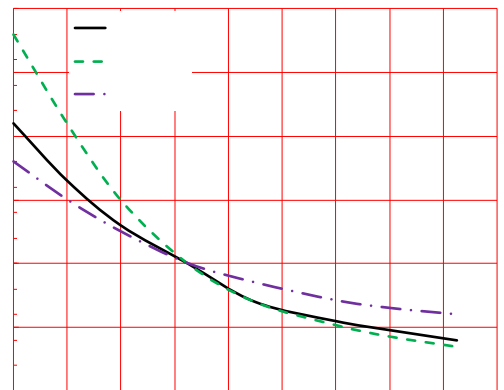
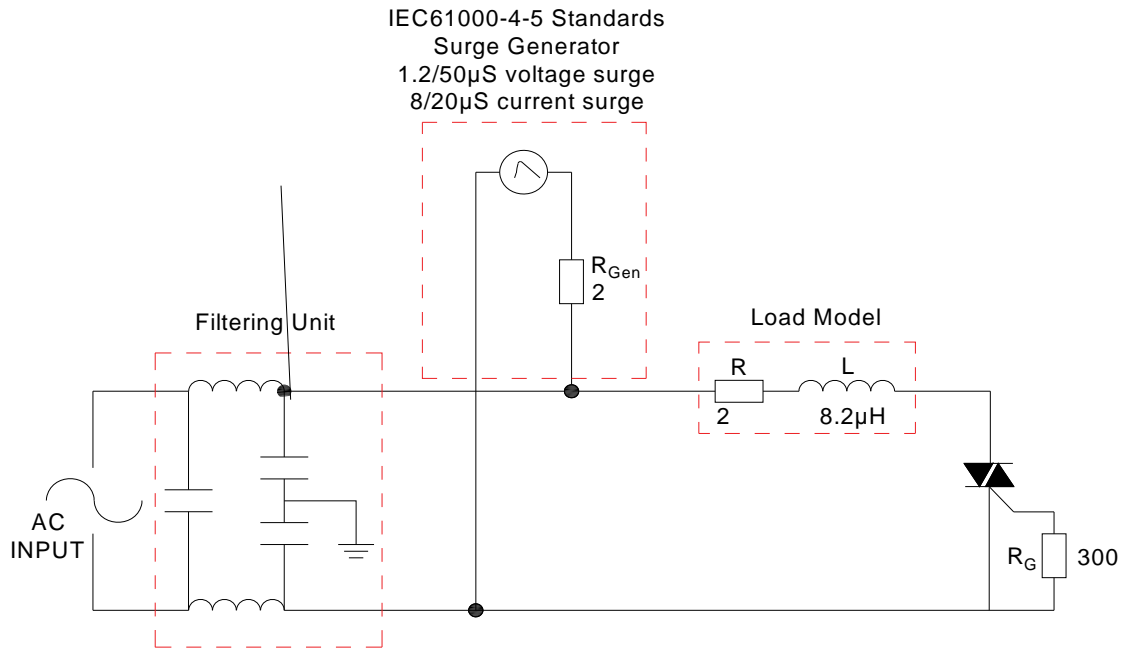




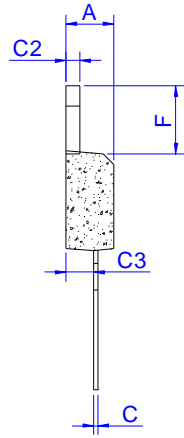
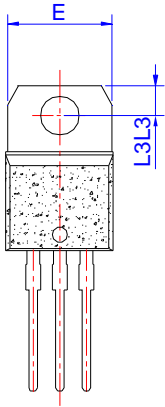
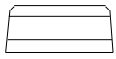
FIG.7: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards





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Date	Revision	Changes
Apr.12, 2023	A.1.0	Last updated





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