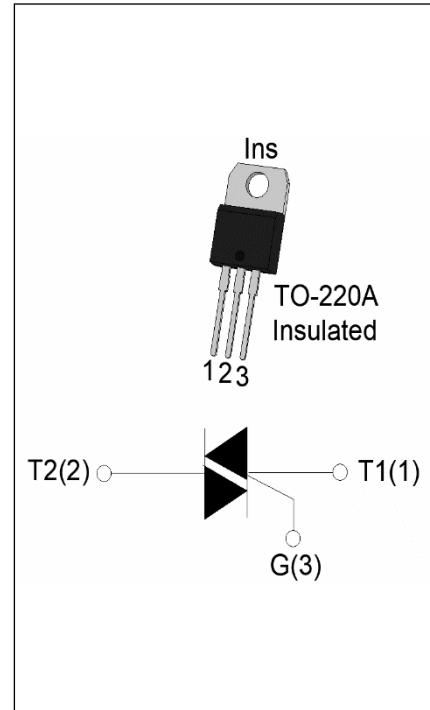




The JST16A-600C triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. By using an internal ceramic pad, JST16A-600C provides a rated insulation voltage of 2500 VRMS, complying with UL standards (File ref: E252906). Package TO-220A is RoHS compliant.

Symbol	Value	Unit
$I_{T(RMS)}$	16	A
V_{DRM}/V_{RRM}	600	V
$I_{GT} / / /$	25/25/25/50	mA



Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage ($T_j=25^\circ C$)	V_{DRM}	600	V
Repetitive peak reverse voltage ($T_j=25^\circ C$)	V_{RRM}	600	V
RMS on-state current ($T_c = 80^\circ C$)	$I_{T(RMS)}$	16	A
Non repetitive surge peak on-state current (full cycle , $t_p=20ms$, $T_j=25^\circ C$)	I_{TSM}	160	A
Non repetitive surge peak on-state current (full cycle , $t_p=16.6ms$, $T_j=25^\circ C$)		176	
I^2t value for fusing ($t_p=10ms$, $T_j=25^\circ C$)	I^2t	128	A^2s
Critical rate of rise of on-state current ($I_G=2mA$, I_{GT} , $f=100Hz$, $T_j=125^\circ C$)	dl/dt	80	$A/\mu s$
		40	
Peak gate current ($t_p=20\mu s$, $T_j=125^\circ C$)	I_{GM}	4	A
Average gate power dissipation ($T_j=125^\circ C$)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	10	W

Peak pulse voltage (T _j =25 °C; non-repetitive, off-state; FIG.7)	V _{pp}	3.	kV
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(T_j=25 °C unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I _{GT}	V _D =12V R _L =33	- -	MAX.	25	mA
				50	
V _{GT}		ALL	MAX.	1	V
V _{GD}	V _D =V _{DRM} T _j =125 °C R _L =3.3K	ALL	MIN.	0.2	V
I _L	I _G =1.2I _{GT}	- -	MAX.	40	mA
				80	
I _H	I _T =500mA		MAX.	40	mA
dV/dt	V _D =400V Gate Open T _j =125 °C		MIN.	800	V/μs
(dV/dt)c	(dI/dt)c=7A/ms, T _j =125 °C		MIN.	6	V/μs

t_{on} I_G=80mA I_A=400mA I_R=40mA

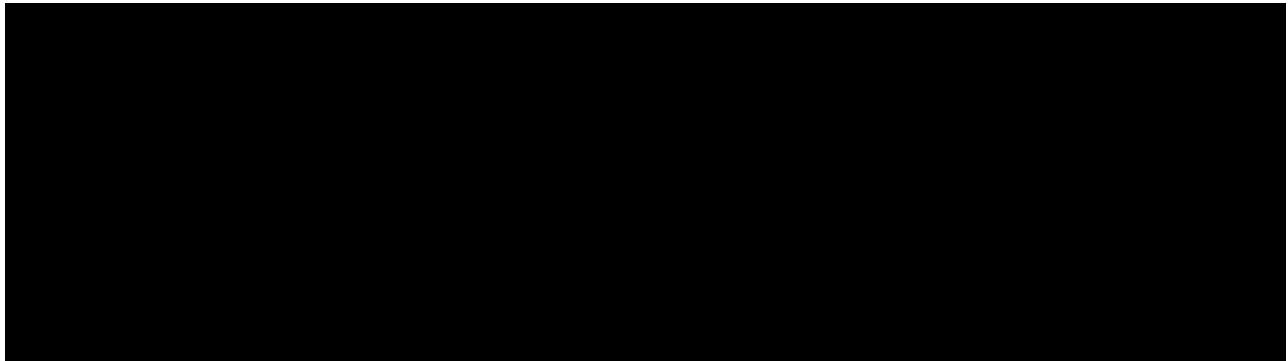


FIG.1 Maximum power dissipation versus RMS on-state current

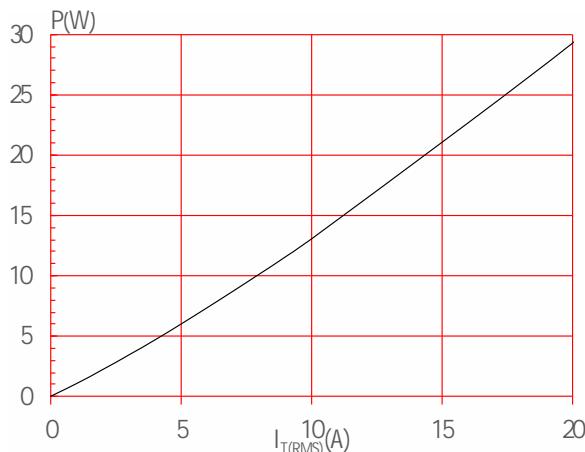


FIG.3: Surge peak on-state current versus number of cycles

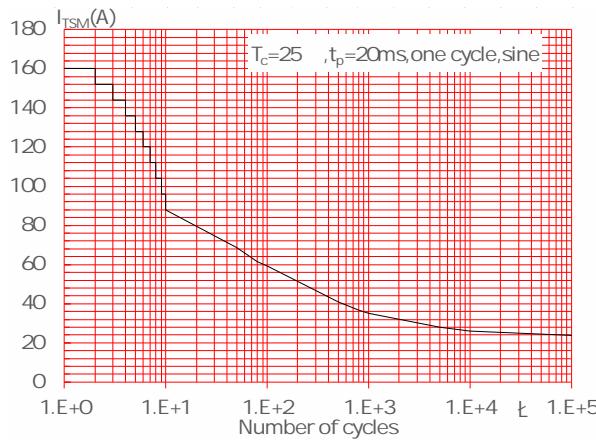


FIG.2: RMS on-state current versus case temperature

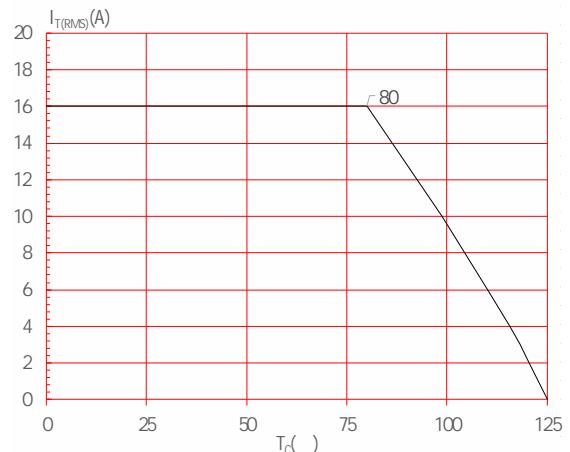


FIG.4: On-state characteristics

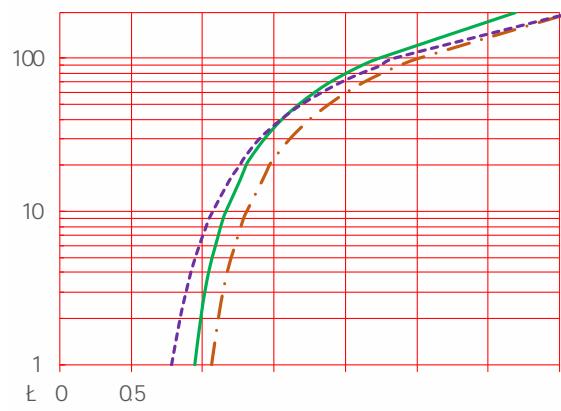
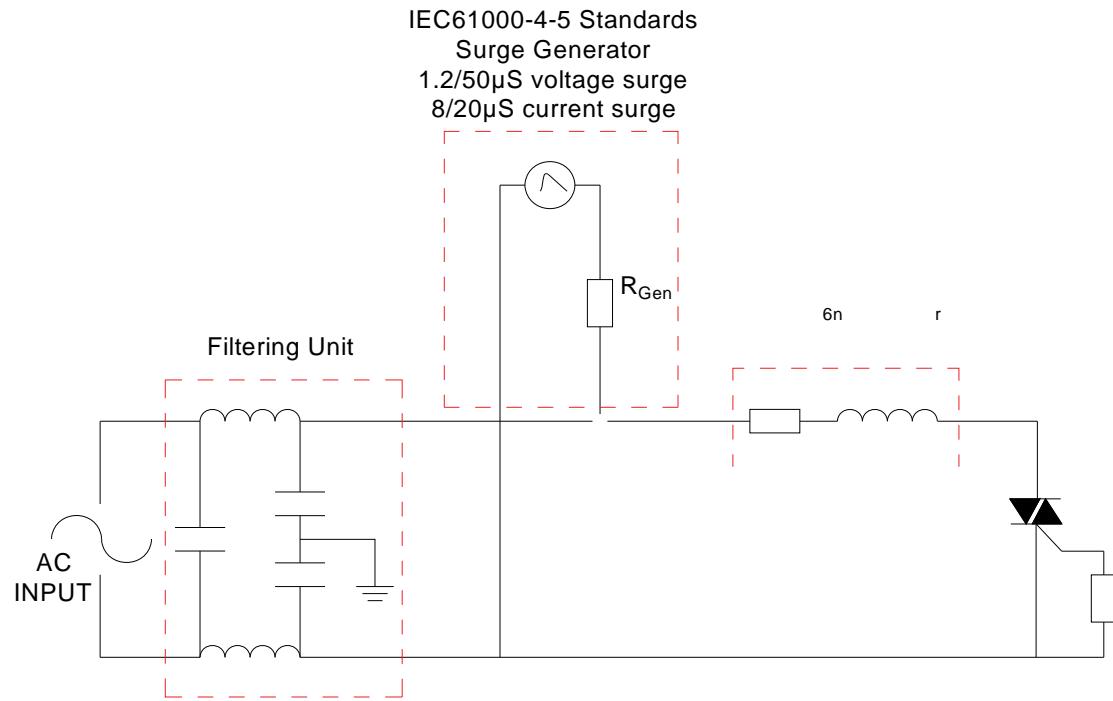


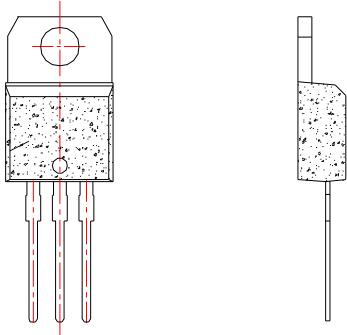
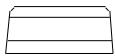
FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		-	-			
JST16A-600C	600	25	50	TO-220A(Ins)	50	Tube

Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated



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