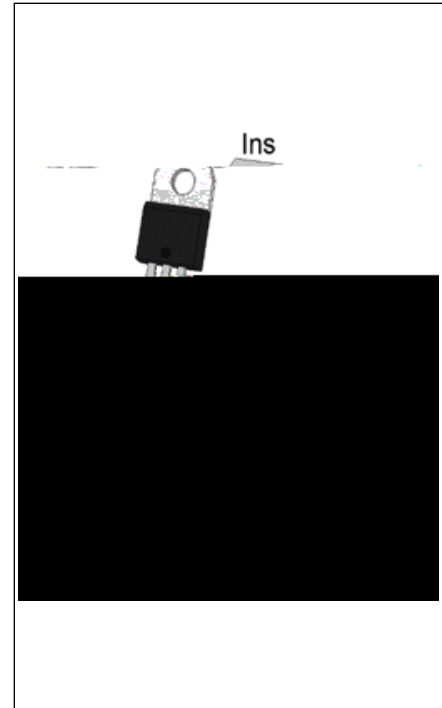


The JST139A-800D triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. By using an internal ceramic pad, JST139A-800D provides a rated insulation voltage of 2500 VRMS, complying with UL standards (File ref: E252906). Package TO-220A is RoHS compliant.



Symbol	Value	Unit
$I_{T(RMS)}$	16	A
V_{DRM}/V_{RRM}	800	V
$I_{GT} / / /$	5/5/5/10	mA

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	800	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	800	V
RMS on-state current ($T_c = 72^\circ\text{C}$)	$I_{T(RMS)}$	16	A
Non repetitive surge peak on-state current (full cycle, $t_p=20\text{ms}$, $T_j=25^\circ\text{C}$)	I_{TSM}	140	A
Non repetitive surge peak on-state current (full cycle, $t_p=16.6\text{ms}$, $T_j=25^\circ\text{C}$)		154	
I^2t value for fusing ($t_p=10\text{ms}$, $T_j=25^\circ\text{C}$)	I^2t	98	A^2s
Critical rate of rise of on-state current ($I_G=2 \times I_{GT}$, $f=100\text{Hz}$, $T_j=125^\circ\text{C}$)	-	50	A/s
	-	20	
Peak gate current ($t_p=20\text{ }\mu\text{s}$, $T_j=125^\circ\text{C}$)	I_{GM}	4	A
Average gate power dissipation ($T_j=125^\circ\text{C}$)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	10	W

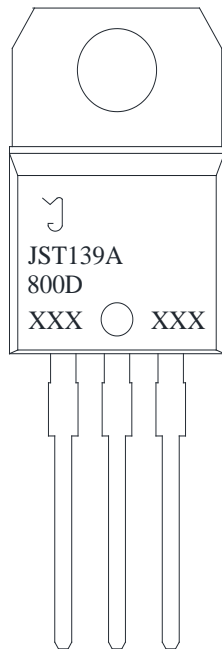
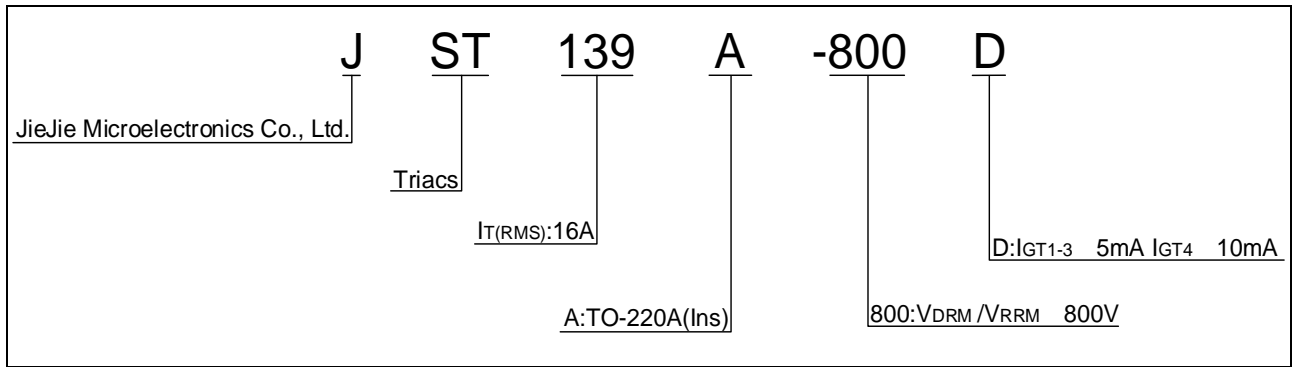


FIG.1 Maximum power dissipation versus RMS on-state current

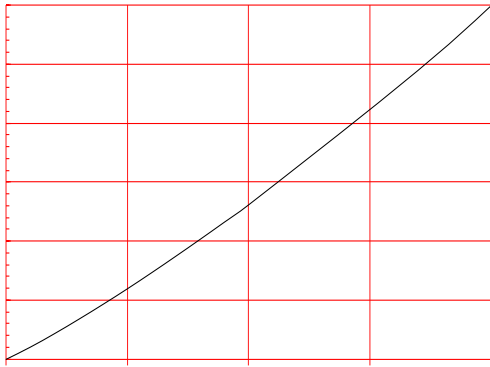


FIG.2: RMS on-state current versus case temperature

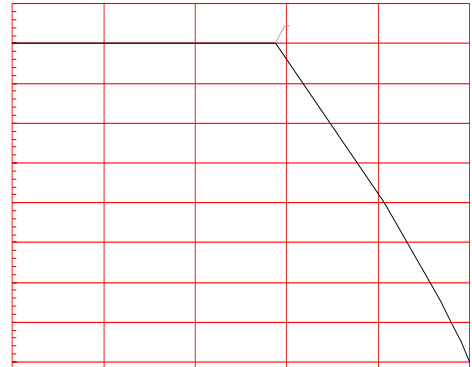


FIG.3: Surge peak on-state current versus number of cycles

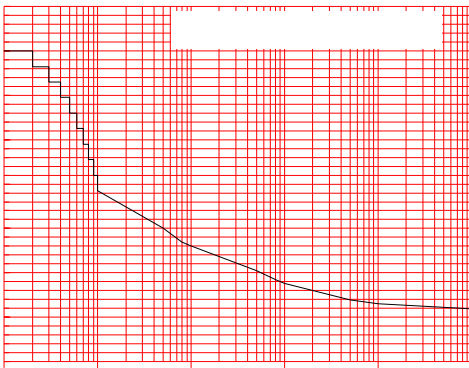


FIG.4: On-state characteristics

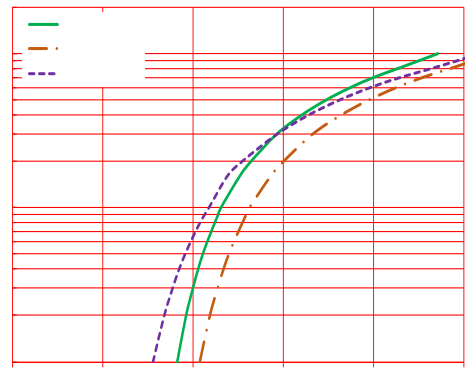


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$, and corresponding value of I^2t
(- : $dI/dt < 50$ t - : $dI/dt < 20$)

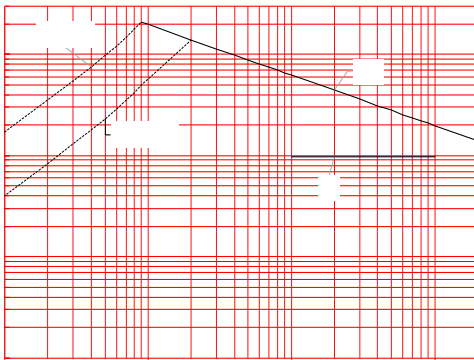


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

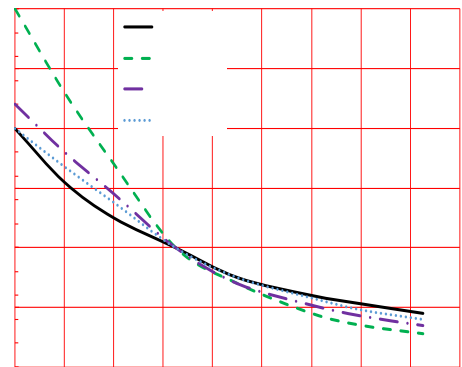
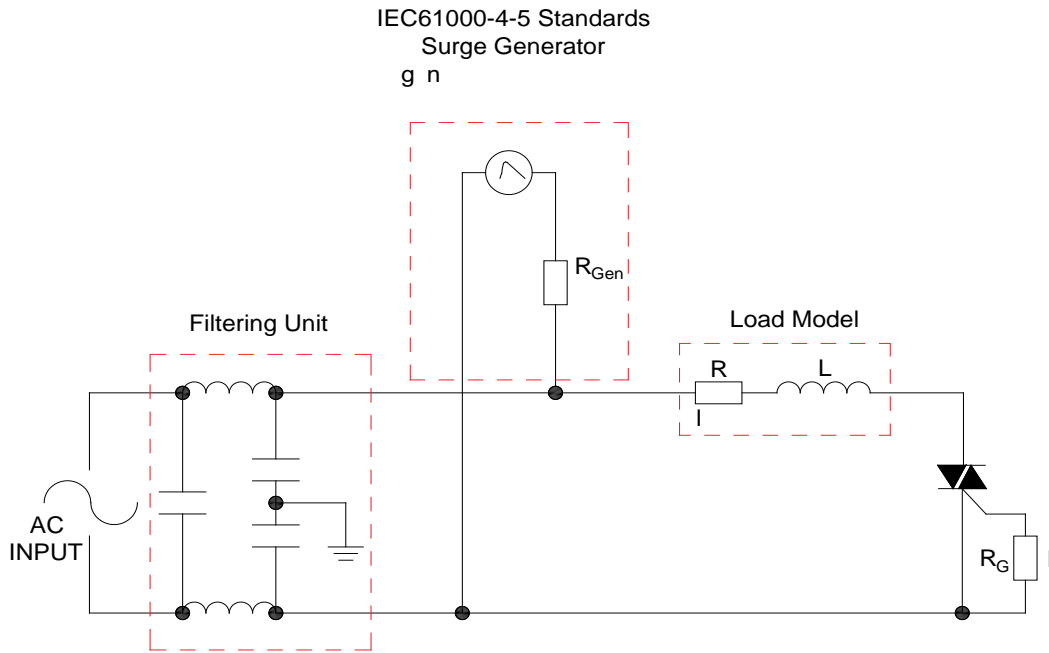


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



Refer to Instructions for installation of plastic-sealed in-line power devices released by JieJie.

Order code	Voltage V _{DRM} /V _{RRM} (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		-	-			
JST139A-800D	800	5	10	TO-220A(Ins)	50	Tube

Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated

