

**JST136V-800D 1A TRIAC**

Rev.A.1.0

DESCRIPTION:

The JST136V-800D triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. Package SOT-223 is RoHS compliant.

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	1	A
V_{DRM}/V_{RRM}	800	V

 I_{GT} **ABSOLUTE MAXIMUM RATINGS**

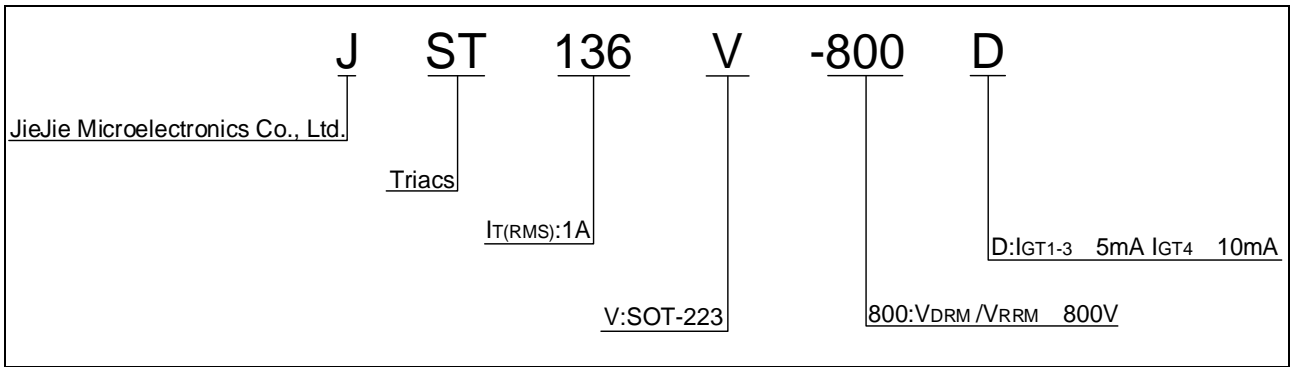
Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	800	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	800	V
RMS on-state current ($T_c=107^\circ\text{C}$)	$I_{T(RMS)}$	1	A
Non repetitive surge peak on-state current (full cycle, $t_p=20\text{ms}$, $T_j=25^\circ\text{C}$)	I_{TSM}	25	A
Non repetitive surge peak on-state current (full cycle, $t_p=16.6\text{ms}$, $T_j=25^\circ\text{C}$)		27.5	
I^2t value for fusing ($t_p=10\text{ms}$, $T_j=25^\circ\text{C}$)	I^2t	3.125	A^2s
Critical rate of rise of on-state current ($I_G=2 I_{GT}$, $f=100\text{Hz}$, $T_j=125^\circ\text{C}$)	di/dt	50	A/s
		40	
Peak gate current ($t_p=20\text{ }\mu\text{s}$, $T_j=125^\circ\text{C}$)	I_{GM}	2	A
Average gate power dissipation ($T_j=125^\circ\text{C}$)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	5	W
Peak pulse voltage ($T_j=25^\circ\text{C}$; non-repetitive, off-state; FIG.8)	V_{pp}	3.5	kV

ELECTRICAL CHARACTERISTICS ($T_j=25$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V R_L=33$	- -	MAX.	5	mA
				10	
V_{GT}		ALL	MAX.	1.3	V
V_{GD}	$V_D=V_{DRM} T_j=125$ $R_L=3.3K$	ALL	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	-	MAX.	15	mA
		-		25	
I_H	$I_T=100mA$		MAX.	15	mA

dV/dt $V_D==$

ORDERING INFORMATION



MARKING

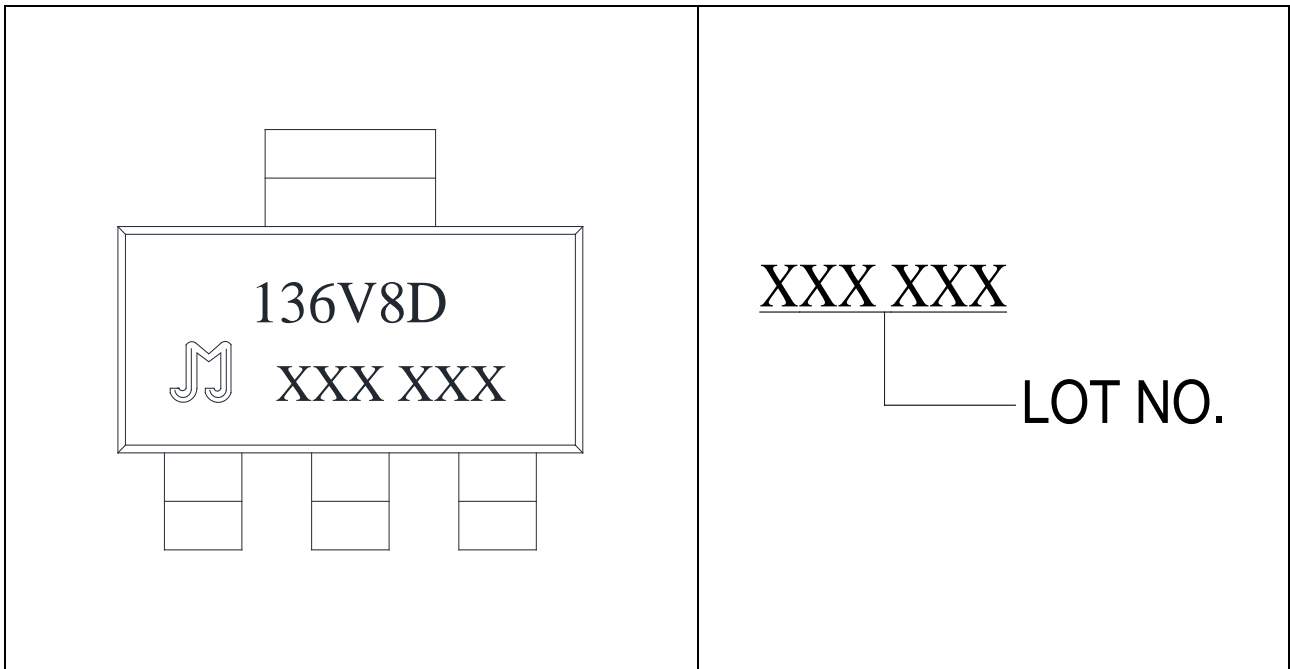


FIG.1 Maximum power dissipation versus RMS on-state current

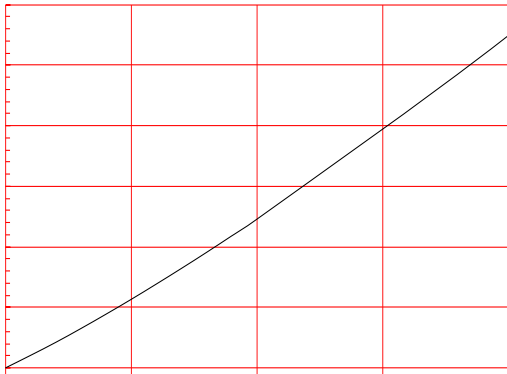


FIG.2: RMS on-state current versus case temperature

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

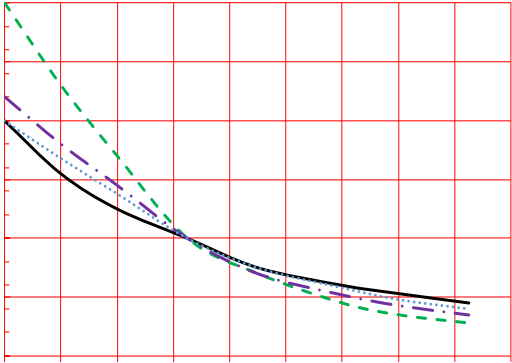


FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards

ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		-	-			
JST136V-800D	800	5	10	SOT-223	4,000	Tape & Reel

Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated

JST136V-800D

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