

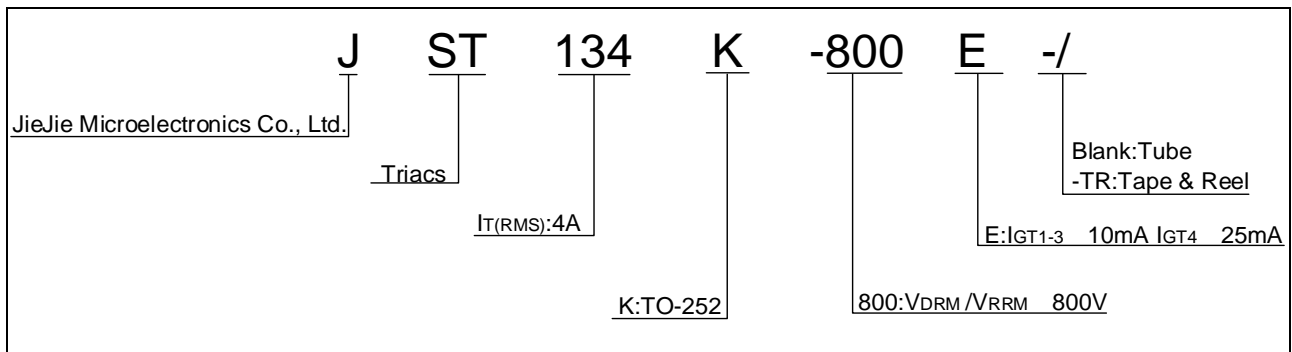


**ELECTRICAL CHARACTERISTICS** ( $T_j=25$  unless otherwise specified)

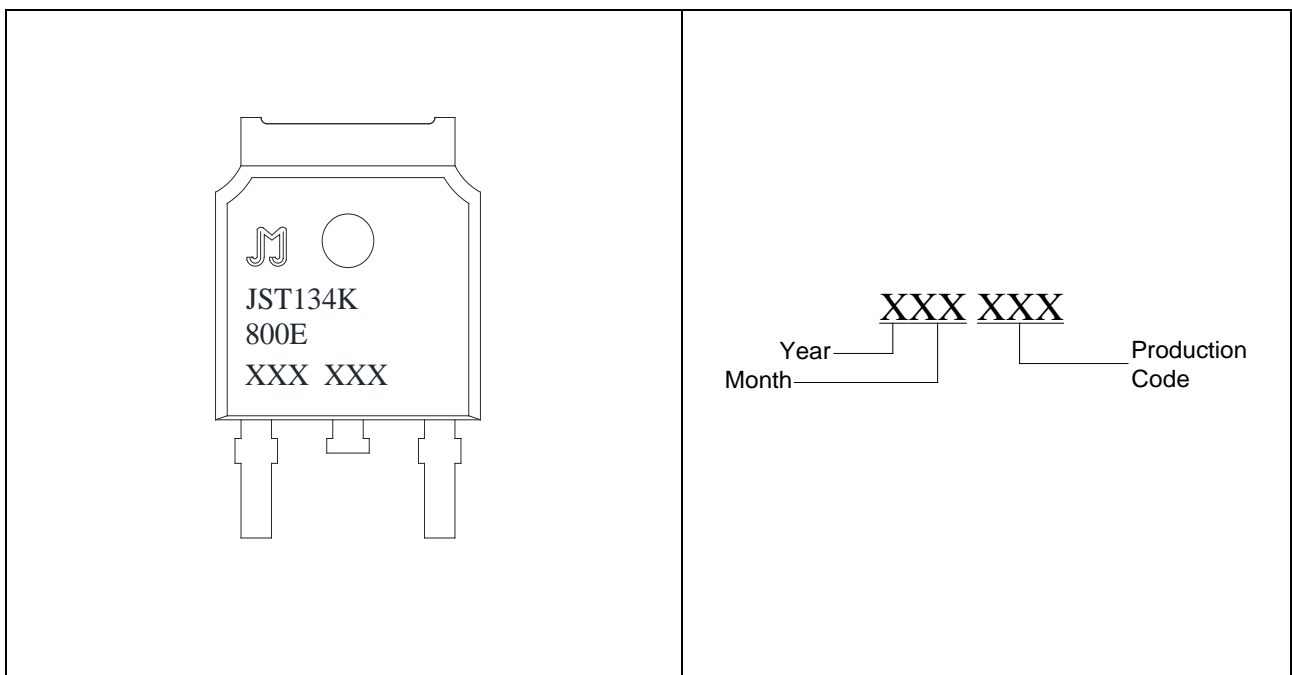
Symbol	Test Condition	Quadrant	Value		Unit
$I_{GT}$	$V_D=12V$ $R_L=33\Omega$	- -	MAX.	10	mA
				25	
$V_{GT}$		ALL	MAX.	1	V
$V_{GD}$	$V_D=V_{DRM}$ $T_j=125$ $R_L=3.3K\Omega$	ALL	MIN.	0.2	V

$I_L$   $I_G=1.2I_{GT}$  - - MAX. 15 mA F" †"DR'b/8)

ORDERING INFORMATION



MARKING



**FIG.1** Maximum power dissipation versus RMS  
on-state current

**FIG.7:** Relative variations of gate trigger current, holding current and latching current versus junction temperature

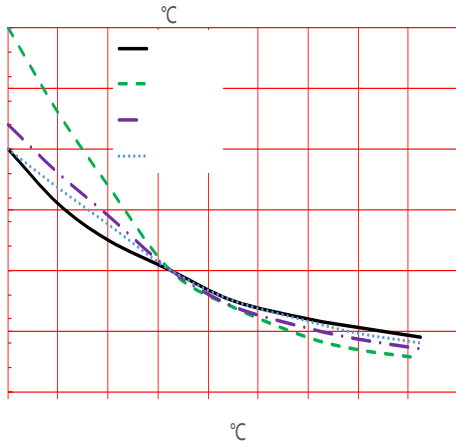
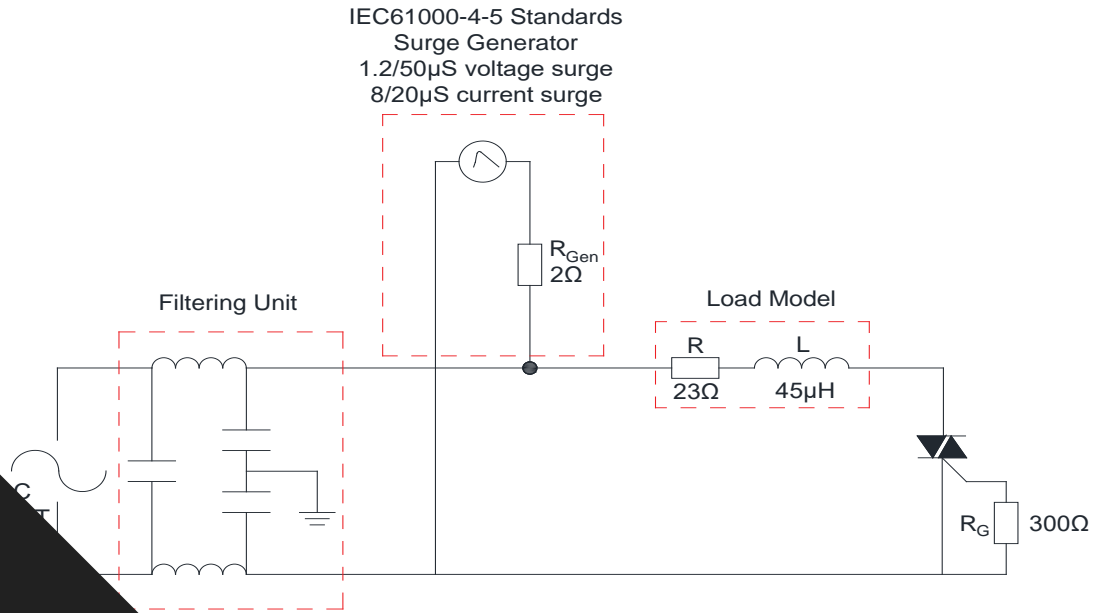


FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



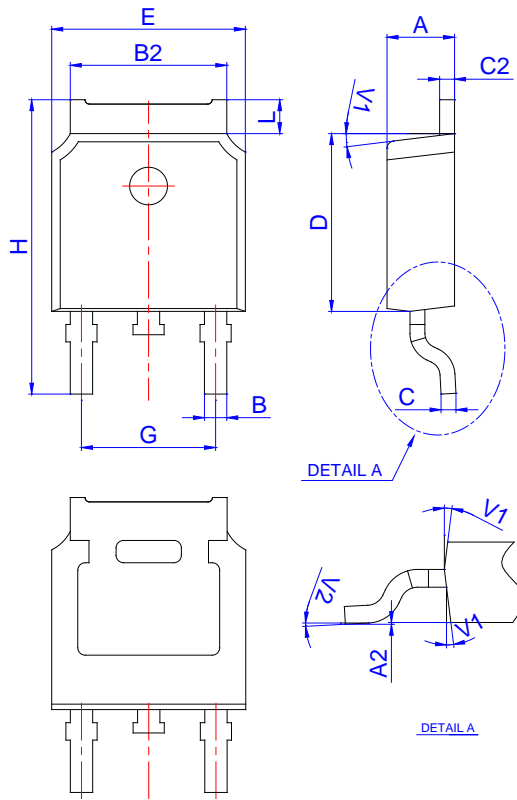
**ORDERING INFORMATION**

Order code	Voltage $V_{DRM}/V_{RRM}$ (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		-	-			
JST134K-800E	800	10	25	TO-252	80	Tube
JST134K-800E-TR					2,500	Tape & Reel

**Document Revision History**

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated

PACKAGE MECHANICAL DATA




Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.15	0		0.006
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1						
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°





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