



JST131U-800DS 1A TRIAC

Rev.A.1.0

The JST131U-800DS triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. Complying with UL standards (File ref: E252906). Package TO-92 is RoHS compliant.

| Parameter | Symbol | Value | Unit |
|--|--------------|---------|--------|
| Storage junction temperature range | T_{stg} | -40-150 | |
| Operating junction temperature range | T_j | -40-125 | |
| Repetitive peak off-state voltage ($T_j=25^\circ C$) | V_{DRM} | 800 | V |
| Repetitive peak reverse voltage ($T_j=25^\circ C$) | V_{RRM} | 800 | V |
| RMS on-state current ($T_c = 44^\circ C$) | $I_{T(RMS)}$ | 1 | A |
| Non repetitive surge peak on-state current (full cycle , $t_p=20ms$, $T_j=25^\circ C$) | I_{TSM} | 16.5 | A |
| Non repetitive surge peak on-state current (full cycle , $t_p=16.6ms$, $T_j=25^\circ C$) | | 18 | |
| I^2t value for fusing ($t_p=10ms$, $T_j=25^\circ C$) | I^2t | 1.36 | A^2s |
| Critical rate of rise of on-state current ($I_G=2 \text{ mA}$, $f=100\text{Hz}$, $T_j=125^\circ C$) | dl/dt | 50 | A s |
| | | 30 | |

| | | | |
|---|-----------------|---|----|
| Peak pulse voltage (T _j =25 °C; non-repetitive, off-state; FIG.7) | V _{PP} | 4 | kV |
|---|-----------------|---|----|

(T_j=25 °C unless otherwise specified)

| Symbol | Test Condition | Quadrant | Value | Unit |
|-----------------|--|----------|-------|------|
| I _{GT} | V _D =12V R _L =33Ω | - - | 5 | mA |
| | | ALL | 10 | |
| V _{GT} | | ALL | 1.3 | V |
| V _{GD} | V _D =V _{DRM} T _j =125 °C R _L =3.3K | ALL | 0.2 | V |
| I _L | I _G =1.2I _{GT} | - - | 5 | mA |
| | | | 20 | |
| I _H | I _T =50mA | MAX. | 7 | mA |

dV/dt V_D=540V Gate Open T_j≠ T

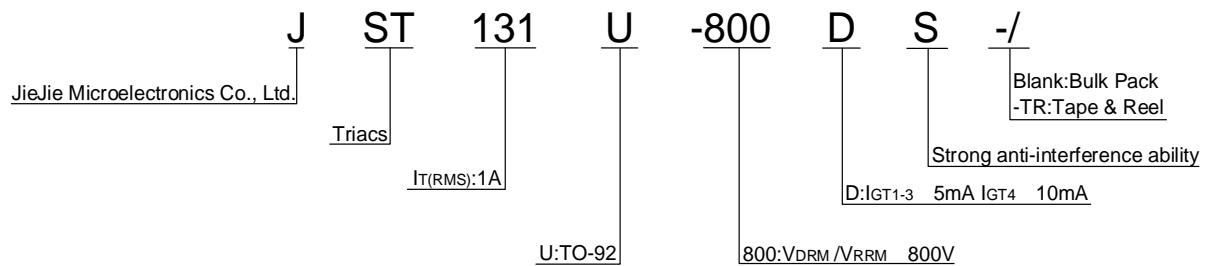


FIG.1 Maximum power dissipation versus RMS on-state current

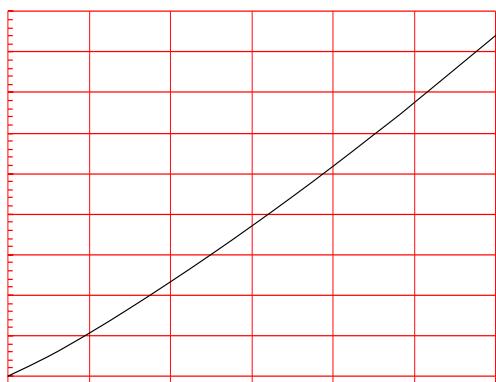


FIG.2: RMS on-state current versus case temperature

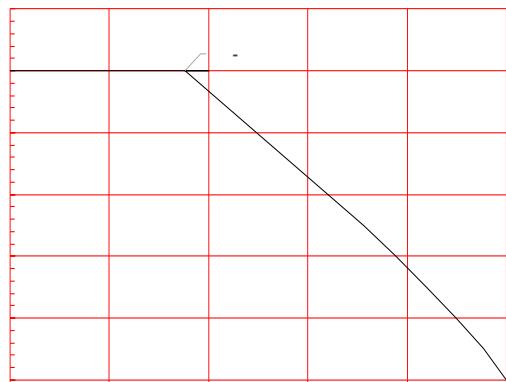


FIG.3: Surge peak on-state current versus number of cycles

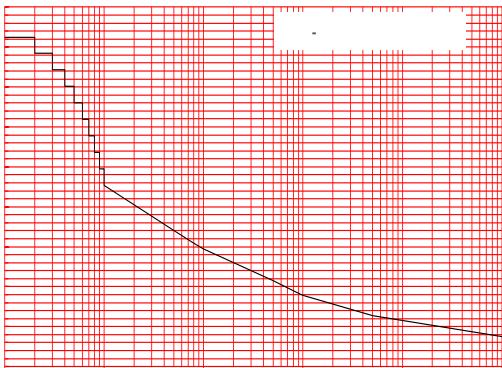


FIG.4: On-state characteristics

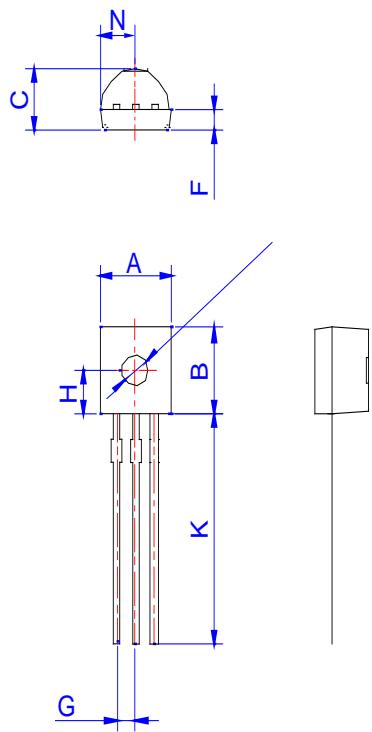


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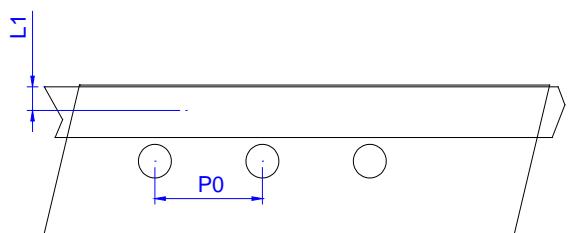
| Order code | Voltage V_{DRM}/V_{RRM} (V) | IGT(mA) | | Package | Base qty. (pcs) | Delivery mode |
|------------------|----------------------------------|---------|----|---------|--------------------|---------------|
| | | H | I | | | |
| JST131U-800DS | 800 | 5 | 10 | TO-92 | 1,000 | Bulk Pack |
| JST131U-800DS-TR | | | | | 2,000 | Tape & Reel |

Document Revision History

| Date | Revision | Changes |
|--------------|----------|--------------|
| Jan.23, 2023 | A.1.0 | Last updated |



| PACKAGE | OUTLINE | BAG (PCS) | INNER BOX (PCS) | CARTON BOX (PCS) |
|---------|-----------|--------------|--------------------|---------------------|
| TO-92 | Bulk Pack | 1,000 | 10,000 | 50,000 |



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