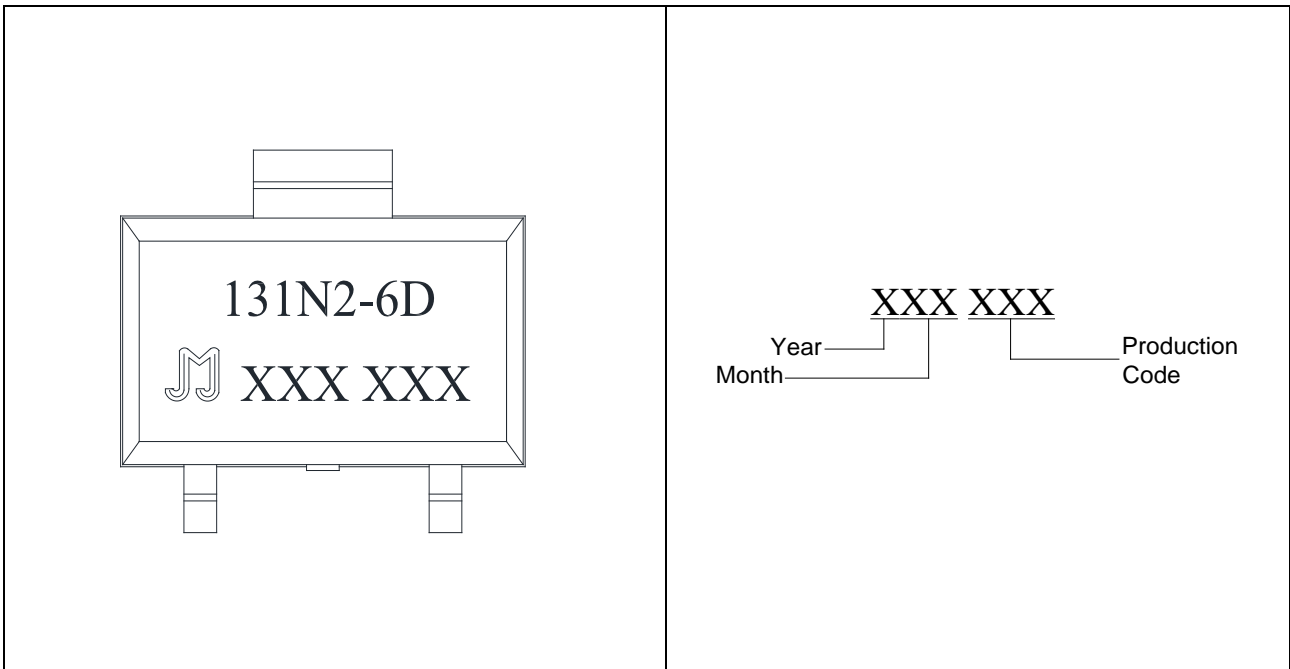
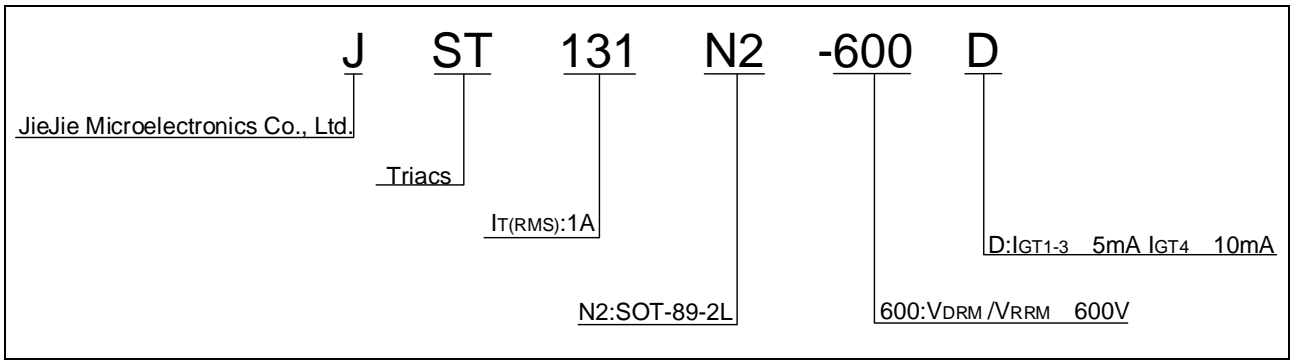




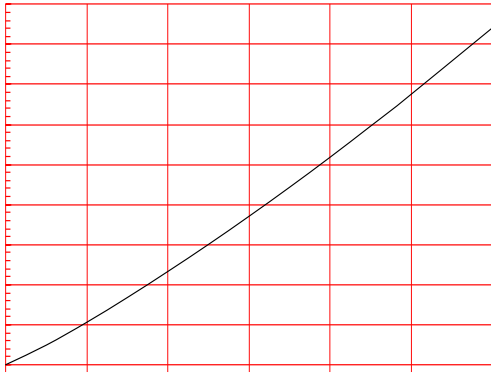
**JST131N2-600D**

(T<sub>j</sub>=25 unless otherwise specified)

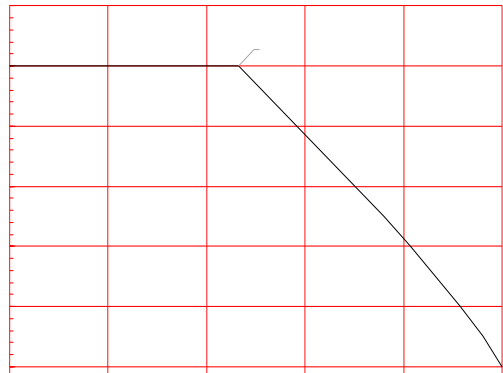
Symbol	Test Condition	Quadrant	Value		Unit
I <sub>GT</sub>	V <sub>D</sub> =12V R <sub>L</sub> =33	- -	MAX.	5	mA
				10	
V <sub>GT</sub>	V <sub>D</sub> =V	ALL	MAX.	1.3	V
V <sub>GD</sub>					



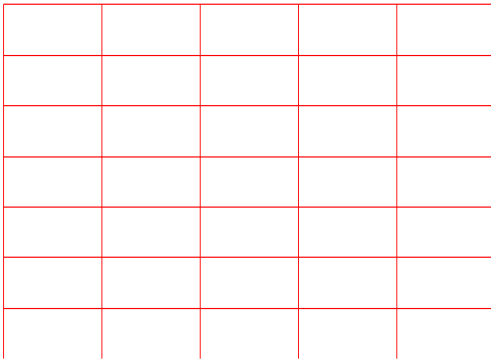
**FIG.1** Maximum power dissipation versus RMS on-state current



**FIG.2:** RMS on-state current versus case temperature



**FIG.3:** RMS on-state current versus ambient temperature (printed circuit board FR4, copper thickness: 35μm) (full cycle)



**FIG.4:** Surge peak on-state current versus number of cycles

**FIG.7:** Relative variations of gate trigger current, holding current and latching current versus junction temperature

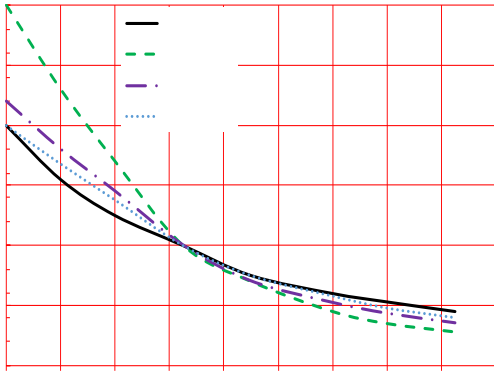
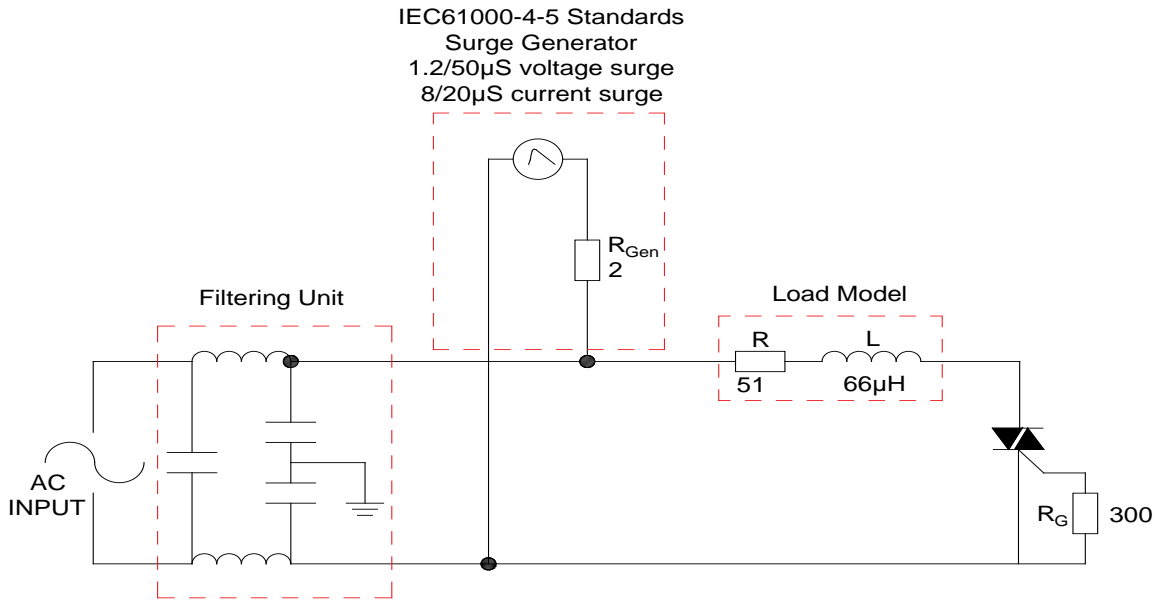


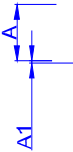
FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



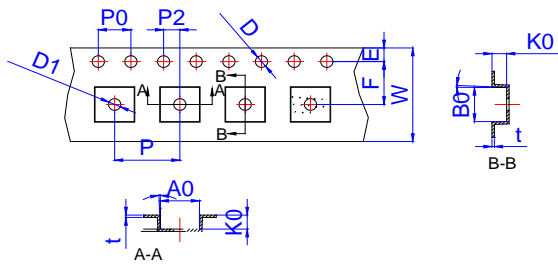
Order code	Voltage $V_{DRM}/V_{RRM}$ (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		-	-			
JST131N2-600D	600	5	10	SOT-89-2L	4,000	Tape & Reel

**Document Revision History**

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated







Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
E	1.65	1.75	1.85	0.065	0.069	0.073
F	5.45	5.50	5.55	0.215	0.217	0.219
P2	1.90	2.00	2.10	0.075	0.079	0.082
D	-	1.50	1.60	-	0.059	0.063
D1	1.50			0.059		
P0	3.90	4.00	4.10	0.154	0.157	0.161
10P0	39.80	40.00	40.20	1.567	1.575	1.583
W			12.30			0.482
P	7.90	8.00	8.10	0.311	0.315	0.319
A0	5.20	5.30	5.40	0.204	0.208	0.212
B0	4.80	4.90	5.00	0.188	0.192	0.196
K0	1.75	1.85	1.95	0.069	0.073	0.076
t	0.20	0.25	0.30	0.008	0.010	0.012
	3°		5°	3°		5°



