

(T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I _{GT}	V _D =12V R _L =33	- -	MAX.	5	mA
				10	
V _{GT}		ALL	MAX.	1.3	V
V _{GD}	V _D =V _{DRM} T _j =125 R _L =3.3K	ALL	MIN.	0.2	V
I _L	I _G =1.2I _{GT}	- -	MAX.	10	mA
				20	
I _H	I _T =50mA		MAX.	7	mA
dV/dt	V _D =540V Gate Open T _j =110		MIN.	50	V s
(dV/dt)c	(dI/dt)c=0.3A/ms, T _j =110		MIN.	5	
t _{on}	I _G =20mA I _A =200mA I _R =20mA T _j =25	TYP.	2.5	s	
t _{off}			25		

Symbol	Parameter		Value(MAX.)	Unit
V _{TM}	I _{TM} =1A t _p =380 s	T _j =25	1.5	V
V _{TO}	Threshold voltage	T _j =125	1.01	V
R _D	Dynamic resistance	T _j =125	316	
I _{DRM}	V _D =V _{DRM} V _R =V _{RRM}	T _j =25	5	A
I _{RRM}		T _j =125	0.25	mA

Symbol	Parameter	Value	Unit
R _{th(j-c)}	junction to case (AC)	65	/W
R _{th(j-a)}	junction to ambient (AC)	150	/W

J	ST	130	U	-800	D	-/
JieJie Microelectronics Co., Ltd.						
	Triacs					
		IT(RMS):0.8A				
			U:TO-92			
					Blank: Bulk Pack -TR: Tape & Reel	
					D: IGT1-3 5mA IGT4 10mA	
					800: V _{DRM} / V _{RRM} 800V	

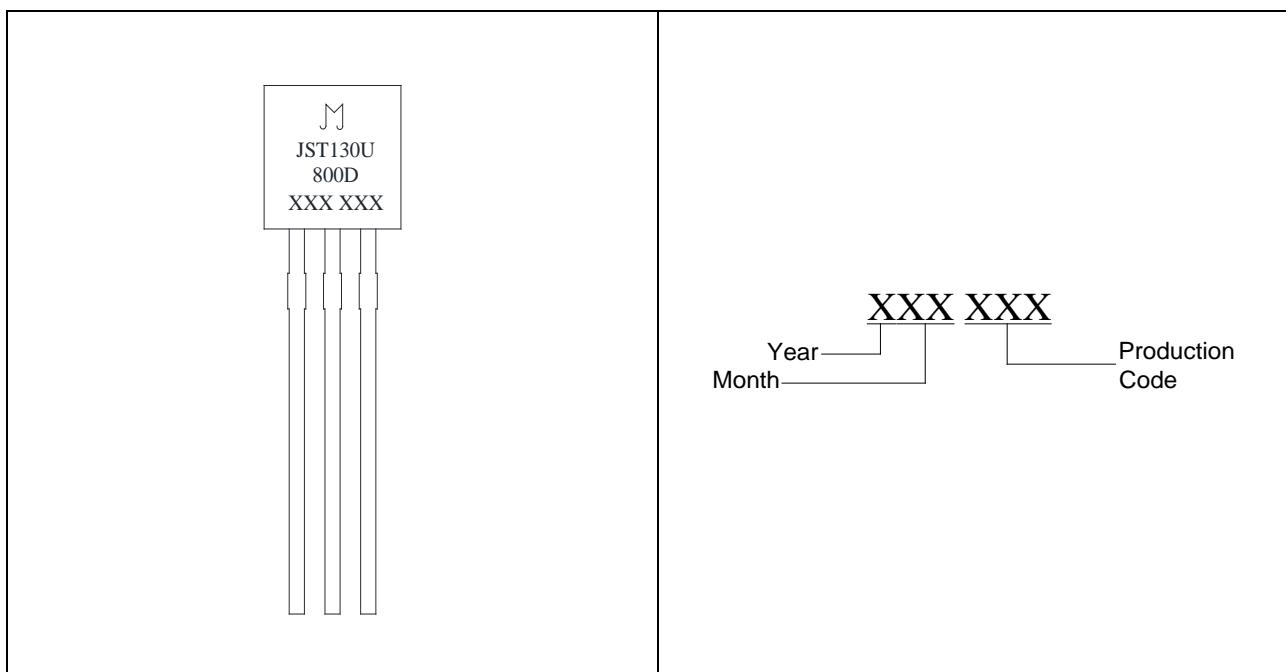


FIG.1 Maximum power dissipation versus RMS on-state current

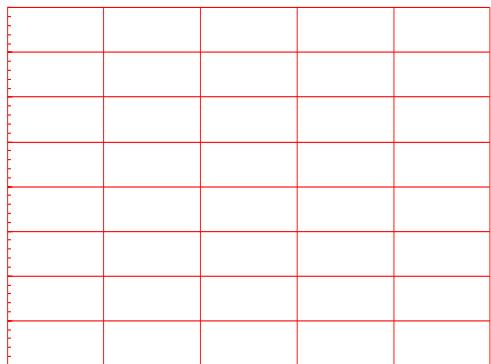
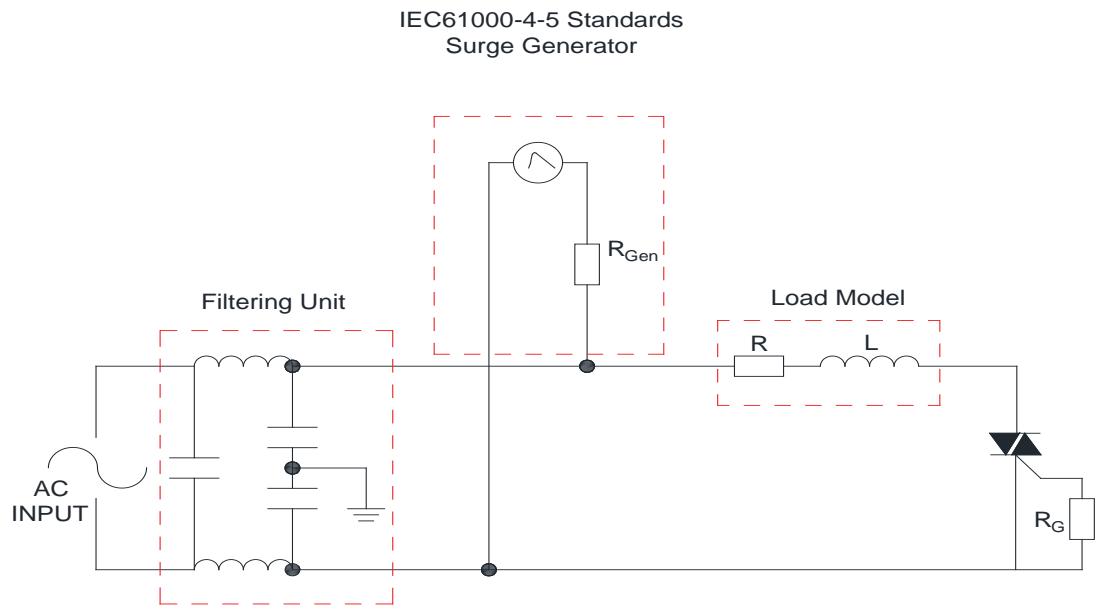


FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards

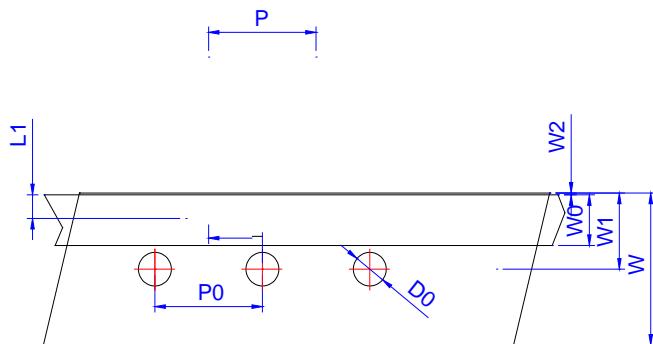


Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		-	-			
JST130U-800D	800	5	10	TO-92	1,000	Bulk Pack
JST130U-800D-TR					2,000	Tape & Reel

Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated

PACKAGE	OUTLINE	BAG (PCS)	INNER BOX (PCS)	CARTON BOX (PCS)
TO-92	Bulk Pack	1,000	10,000	50,000



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