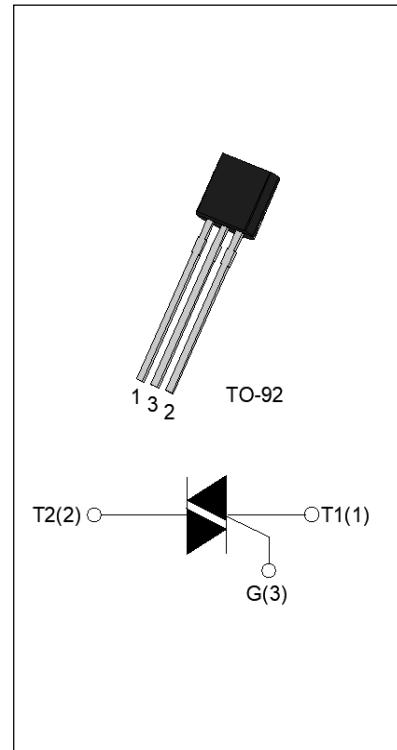




The JST02U-1000SW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. JST02U-1000SW snubberless triac is especially recommended for use on inductive loads. It can be driven directly through the MCU I/O port. Package TO-92 is RoHS compliant.



Symbol	Value	Unit
$I_{T(RMS)}$	2	A
V_{DRM}/V_{RRM}	1000	V
$I_{GT} / /$	10/10/10	mA

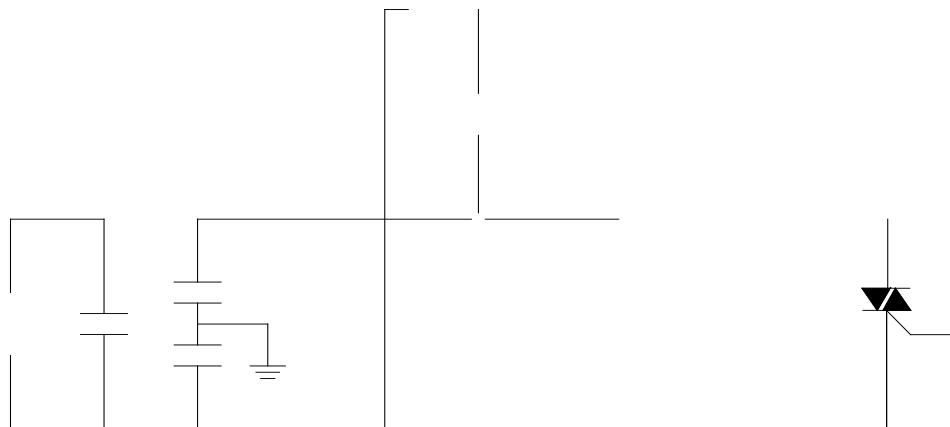
Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage ($T_j=25^\circ C$)	V_{DRM}	1000	V
Repetitive peak reverse voltage ($T_j=25^\circ C$)	V_{RRM}	1000	V
RMS on-state current	$I_{T(RMS)}$	2	A
Non repetitive surge peak on-state current (full cycle, $t_p=20ms$, $T_j=25^\circ C$)	I_{TSM}	25	A
Non repetitive surge peak on-state current (full cycle, $t_p=16.6ms$, $T_j=25^\circ C$)		27.5	
I^2t value for fusing ($t_p=10ms$, $T_j=25^\circ C$)	I^2t	3.7	A^2s
Critical rate of rise of on-state current ($I_G=2mA$, I_{GT} , $f=100Hz$, $T_j=125^\circ C$)	dI/dt	100	A/s
Peak gate current ($t_p=20ms$, $T_j=125^\circ C$)	I_{GM}	2	A
Average gate power dissipation ($T_j=125^\circ C$)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	10	W

Peak pulse voltage
($T_j=25^\circ C$; non-

JST02U-1000SW

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FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards

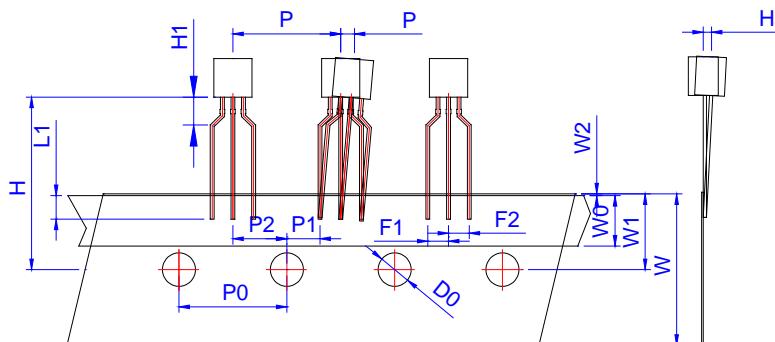


Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
JST02U-1000SW	1000	10	TO-92	1,000	Bulk Pack
JST02U-1000SW-TR				2,000	Tape & Reel

Document Revision History

Date	Revision	Changes
Apr.10, 2023	A.1.0	Last updated

PACKAGE	OUTLINE	BAG (PCS)	INNER BOX (PCS)	CARTON BOX (PCS)
TO-92	Bulk Pack	1,000	10,000	50,000



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
P	12.40	12.70	13.00	0.488	0.500	0.512
P0	12.40	12.70	13.00	0.488	0.500	0.512
P1	3.55	3.85	4.15	0.140	0.152	0.163
P2	5.95	6.35	6.75	0.233	0.250	0.265
P	-1.0	0	1.0	-0.039	0	0.039
F1 F2	2.30	2.50	2.70	0.090	0.098	0.106
F1-F2	-0.1	0	0.1	-0.004	0	0.004
W	17.50	18.00	19.00	0.689	0.709	0.748
W0	5.50	6.00	6.50	0.217	0.236	0.256
W1	8.50	9.00	9.50	0.335	0.354	0.374
W2			1.0			0.039
D0	3.80	4.0	4.20	0.150	0.157	0.165
H	-1.0	0	1.0	-0.039	0	0.039
L1	2.5			0.098		
H	18.0	19.0	20.0	0.709	0.748	0.787
H1			2.70			0.106

PACKAGE	OUTLINE	REEL (PCS)	INNER BOX (PCS)	CARTON BOX (PCS)
TO-92	Tape & Reel	/	2,000	20,000

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