Peak gate current (t_p=20μs , T_j=150)	I_{GM}	5	A
Average gate power dissipation (T_j=150)	$P_{G(AV)}$	1	W
Peak gate power	P_{GM}	20	W
Peak pulse voltage (T_j=25 ; non-repetitive,off-state;FIG.7)	V_{pp}	0.5	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Value			Unit
		MIN.	TYP.	MAX.	
I_{GT}	V_D=12V R_L=33Ω	-	-	15	mA
V_{GT}		-	-	1	V
V_{GD}	V_D=V_{DRM} T_j=150 R_L=3.3KΩ	0.2	-	-	V
I_L	I_G=1.2I_{GT}	-	-	60	mA
I_H	I_T=500mA	-	-	50	mA
dV/dt	V_D=540V Gate Open T_j=150	150	-	-	V/μs
t_{on}	I_G=40mA I_A=400mA I_R=40mA T_j=25	-	5	-	μs
t_{off}		-	70	-	

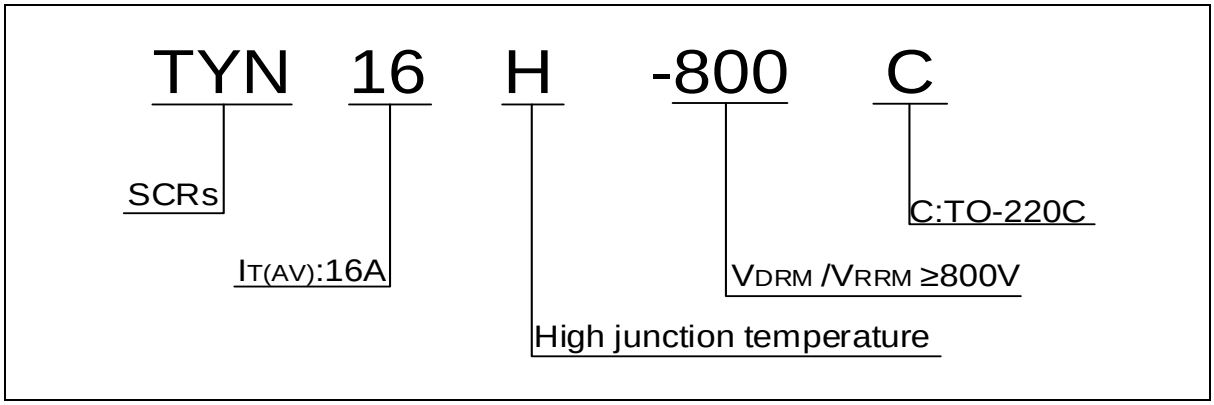
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	I_{TM}=40A t_p=380μs	T_j=25	1.55	V
V_{TO}	Threshold voltage	T_j=150	0.73	V
R_D	Dynamic resistance	T_j=150	23	mΩ
I_{DRM}	V_D=V_{DRM} V_R=V_{RRM}	T_j=25	5	μA
I_{RRM}		T_j=150	0.5	mA

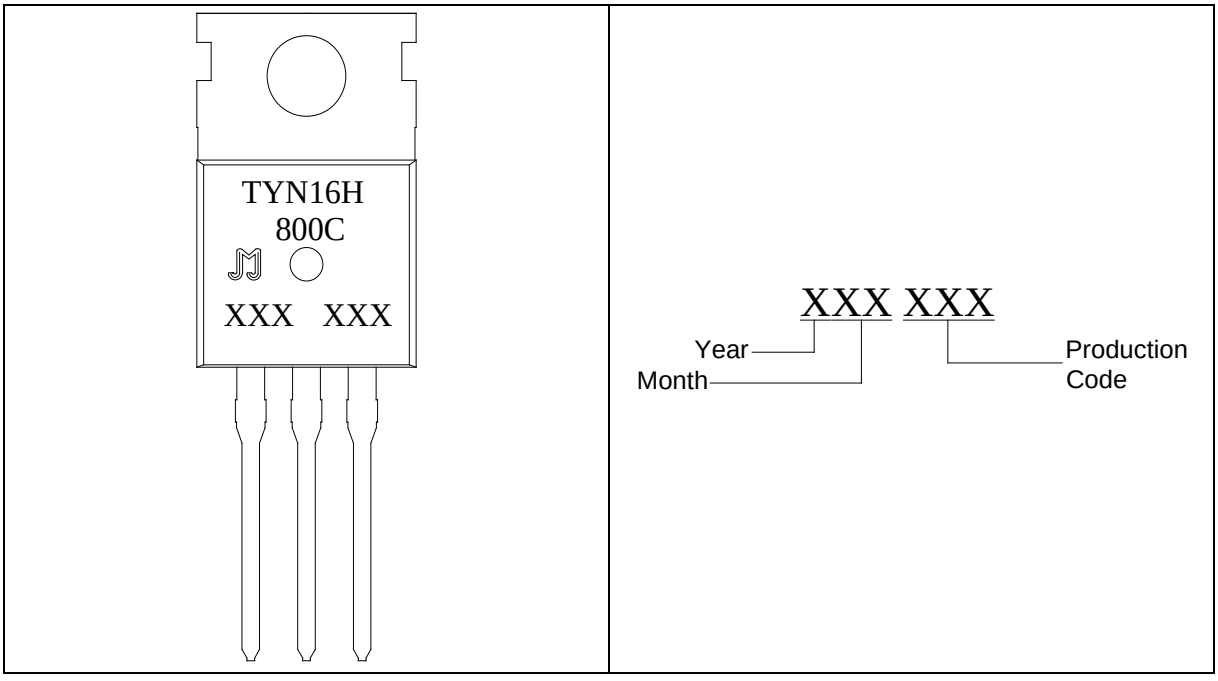
THERMAL RESISTANCES

Symbol

ORDERING INFORMATION

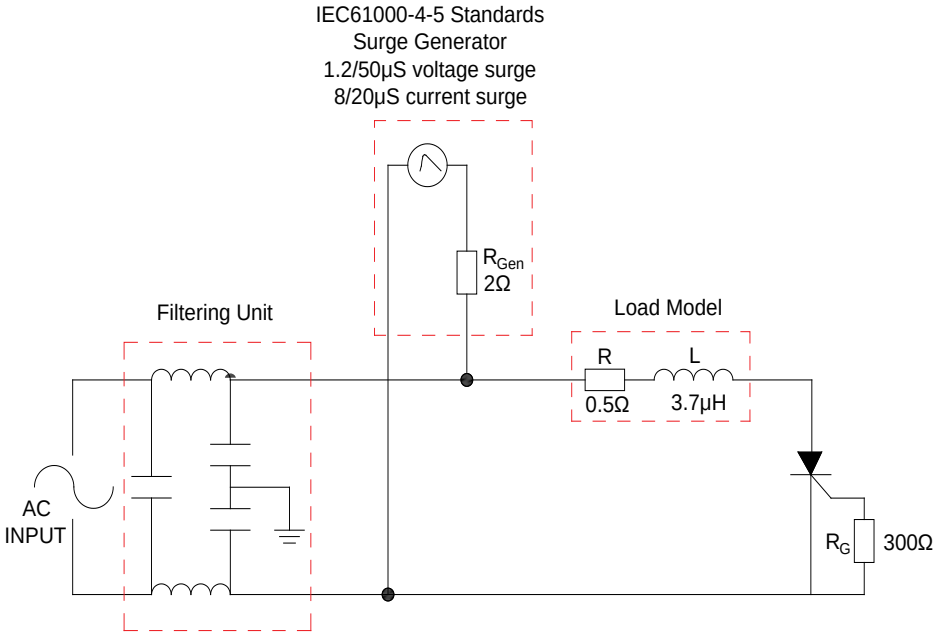


MARKING



TYN

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards.



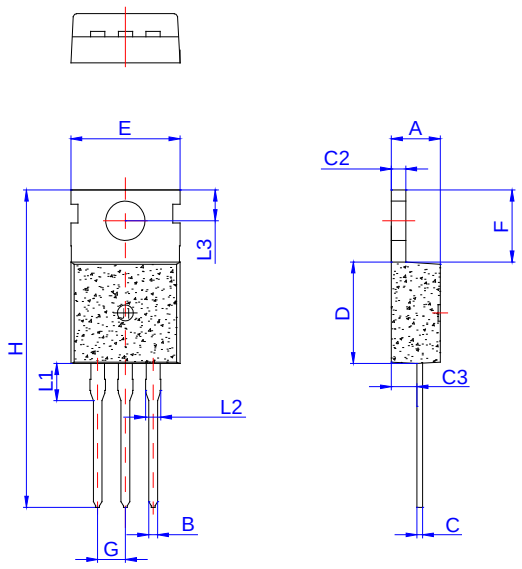
ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
TYN16H-800C	800	15	TO-220C	50	Tube

Document Revision History

Date	Revision	Changes
Apr.13, 2023	A.1.0	Last update

PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
B	0.70		0.90	0.028		0.035
C	0.45		0.60	0.018		0.024
C2	1.25		1.35	0.049		0.053
C3	2.20		2.60	0.087		0.102
D	8.90		9.90	0.350		0.390
E	9.90		10.3	0.390		0.406
F	6.30		6.90	0.248		0.272
G	2.40			0.094		
H	28.0		29.8	1.102		1.173
L1	2.70					0.130
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116

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