

DESCRIPTION:

The T3050H6B-US triac is suitable for general purpose AC switching. It can be used as an ON/OFF switch in applications such as heating regulation, induction motor starting circuits, light dimmers, motor speed controllers. Compared to traditional triacsO50H6B-US provides a very high switching capability up to junction temperatures of 150°C. Package TQ220B is RoHS compliant.

·e	TO-220B Non-Insulated	
	T2(2) O T1(1) G(3)	

MAIN FEATURES	FF	Unit
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I _{T(RMS)}	30	А
V _{DRM} /V _{RRM}	600	V
lgт / /	50/50/50	mA

ABSOLUTE MAXIMUMRATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T _{stg}	-40150	
Operatingjunction temperature range	Tj	-40150	
Repetitive peak of state voltage (₹25)	V_{DRM}	600	V
Repetitive peak reverse voltagej=(25)	V _{RRM}	600	V
RMS on-state current (TO119)	I _{T(RMS)}	30	Α
Non repetitive surge peak shate current (full cycle ,p ≢ 20ms ,	I=	270	А
Non repetitive surge peak shate current (full cycle ,p ≢ 16.6ms , Ţ=25)	lтsм	297	A
I ² t value for fusing ₄ ¶0ms , Ţ=25)	I ² t	365	A ² s
Critical rate of rise of-somate current (IG=2x IGT, f=100Hz, j=150)	dl/dt	100	A s
Peak gate current _p (±20 s , T _j =150)	I _{GM}	4	Α
Average gate power dissipation=(150)	P _{G(AV)}	1	W



Peak gate power	P _{GM}	10	W
Peak pulse voltage	V_{pp}	1.2	kV
(T _i =25; non-repetitive,ofstate;FIG.7)	• pp		

ELECTRICAL CHARACTERISTICS (Tj=25 unless otherwises pecified)

Symbol	Test Condition	Quadrant	Value		Unit
Іст	V _D =12V R =33		MAX.	50	mA
V _{GT}			MAX.	1.3	V
V _{GD}	$V_D = V_{DRM} T_j = 150$ $R_L = 3.3 K$		MIN.	0.15	V

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ORDERING INFORMATION

MARKING

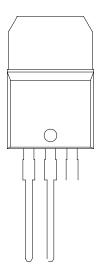




FIG.1 Maximum power dissipation versus RMS onstate current

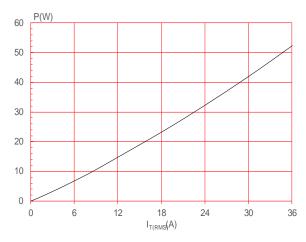


FIG.3: Surge peak onstate current versus number of cycles

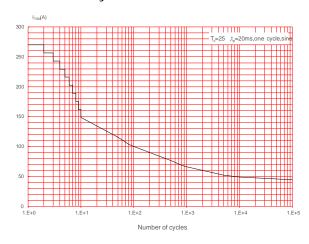


FIG.5: Non-repetitive surge peak estate current for a sinusoidal pulse with width tp<20ms, and corresponding value of 2 t (dl/dt<100As/)

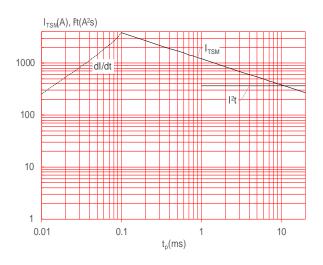


FIG.2: RMS on-state current versus case temperature

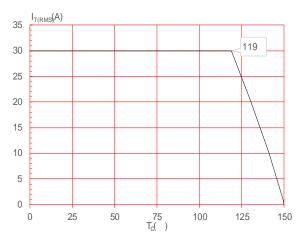


FIG.4: On-state characteristics

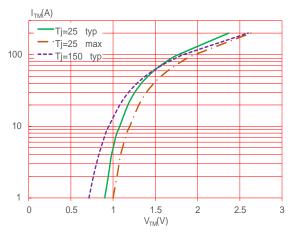


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

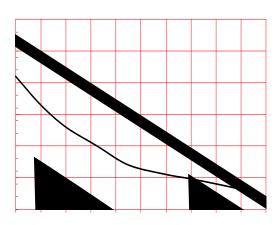
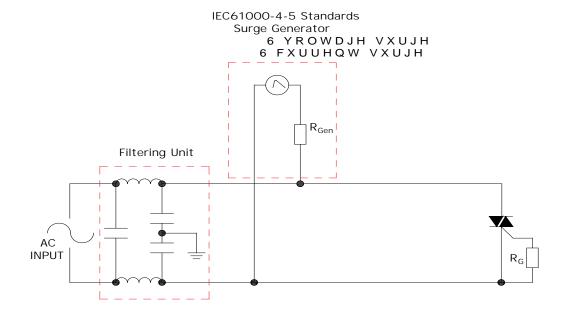




FIG.7 Örest circuit for inductive and resistive loads according C-610004-5 standards



LEAD FORMING AND SOLDERING

Refer to the application not seembly Instructions for Thyristors in Through-Package released by JieJie Microelectronics



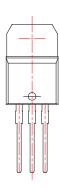
ORDERING INFORMATION

Order code	Voltage V _{DRM} /V _{RRM} (V)	IGT (mA)	Package	Base qty. (pcs)	Delivery mode
T3050H6B-US	600	50	TO-220B	50	Tube



PACKAGE MECHANICAL DATA









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