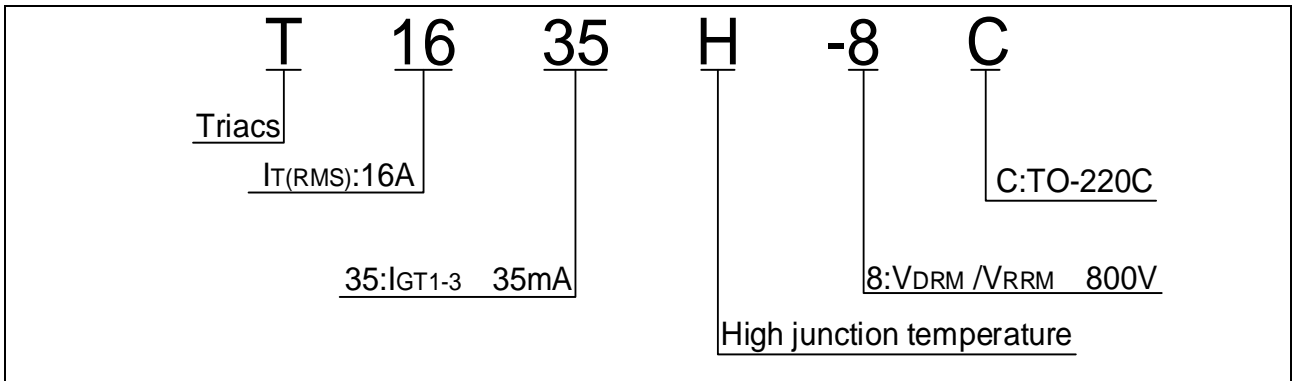


Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.7)	V_{pp}	4	kV
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ELECTRICAL CHARACTERISTICS ($T_j=25$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V$ $R_L=33$	- -	MAX.	35	mA
V_{GT}		A -- -	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=150$				

ORDERING INFORMATION



MARKING

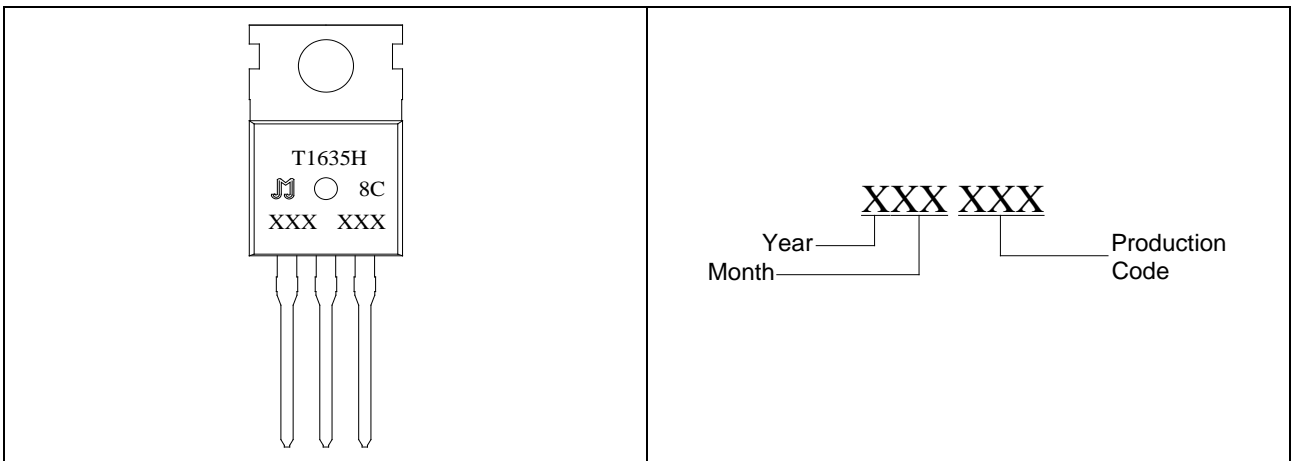


FIG.1 Maximum power dissipation versus RMS on-state current

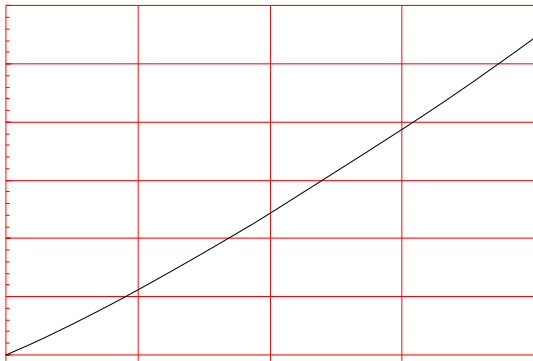


FIG.2: RMS on-state current versus case temperature

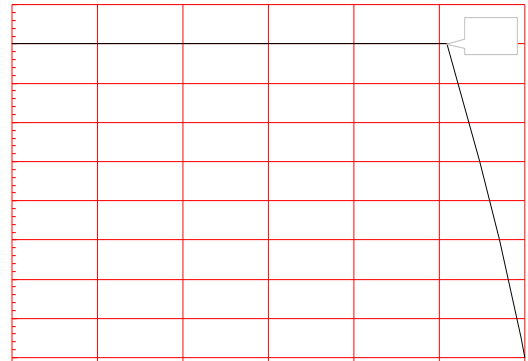


FIG.3: Surge peak on-state current versus number of cycles

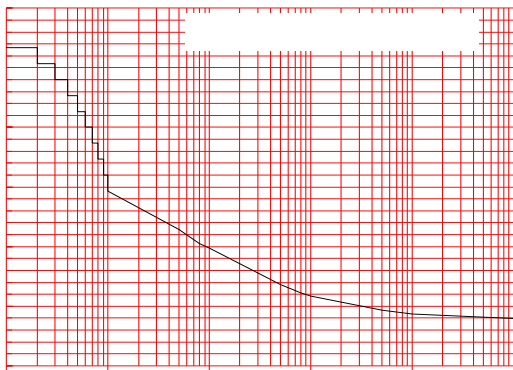


FIG.4: On-state characteristics

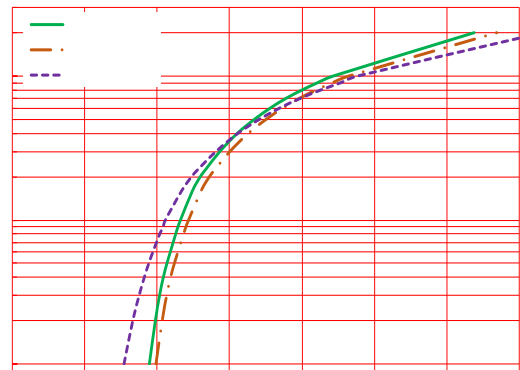
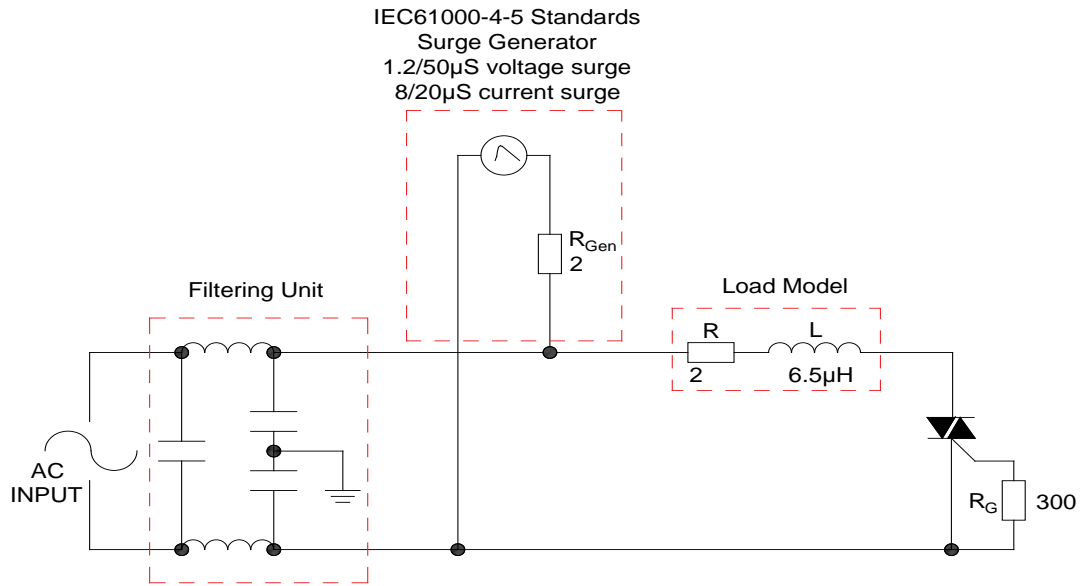


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$, and corresponding value of I^2t ($di/dt < 100\text{A}/\mu\text{s}$)



FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



SHAPING AND SOLDERING PARAMETERS

Refer to Instructions for installation of plastic-sealed in-line power devices released by JieJie

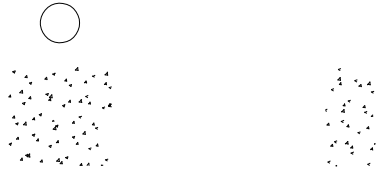
ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
T1635H-8C	800	35	TO-220C	50	Tube

Document Revision History

Date	Revision	Changes
Apr.10, 2023	A.1.0	Last updated

PACKAGE MECHANICAL DATA



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