

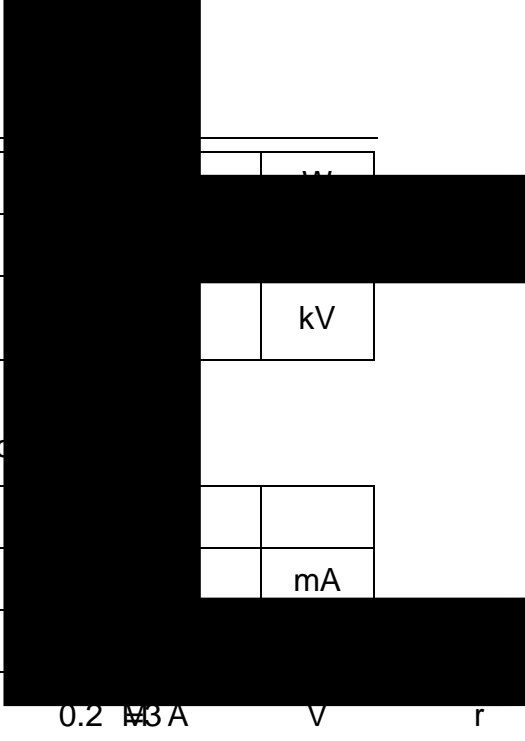


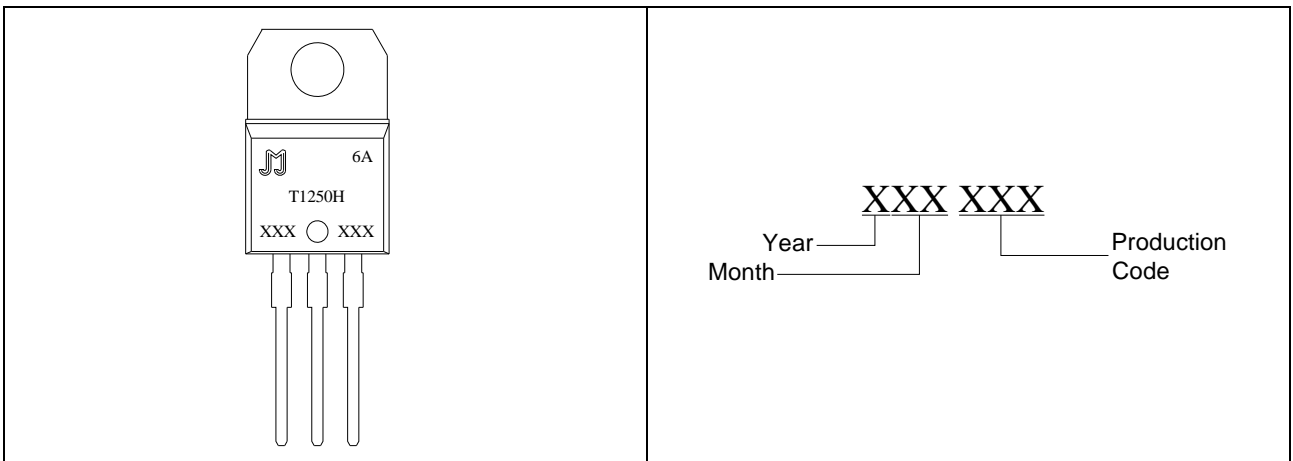
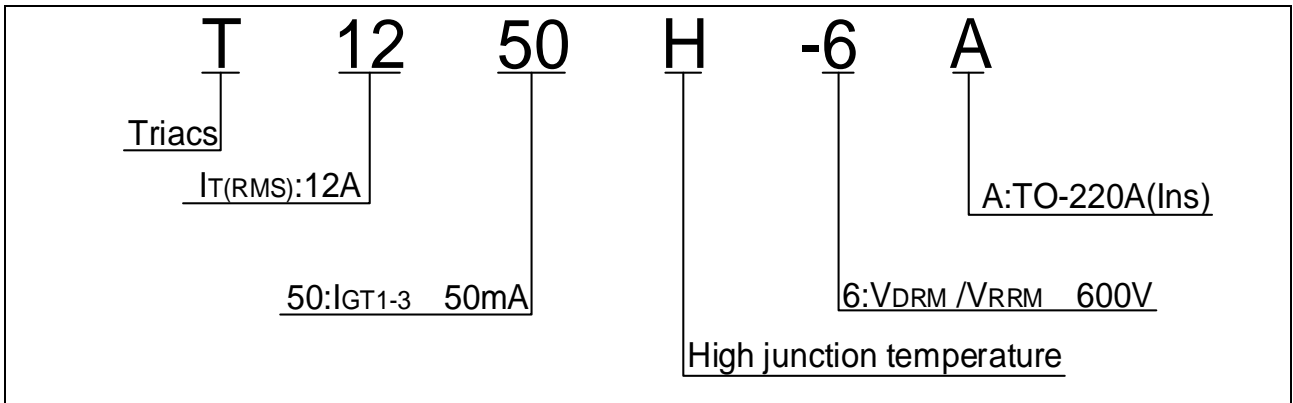


|  |             |  |
|--|-------------|--|
| Average gate power dissipation ( $T_j=150$ )                         | $P_{G(AV)}$ |  |
| Peak gate power  | $P_{GM}$    |  |
| Peak pulse voltage<br>( $T_j=25$ ; non-repetitive, off-state; FIG.7) | $V_{pp}$    |  |

( $T_j=25$  unless otherwise specified)

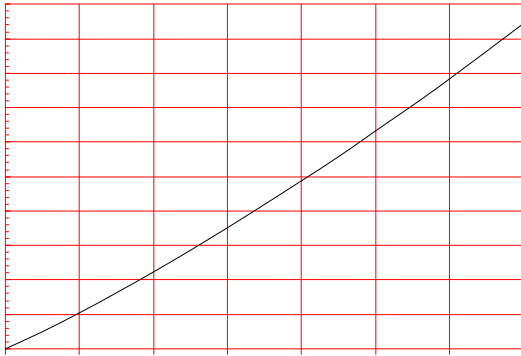
|          |                                     |     |      |
|----------|-------------------------------------|-----|------|
|          |                                     |     |      |
| $I_{GT}$ | $V_D=12V R_L=33$                    | - - | MAX. |
| $V_{GT}$ |                                     | - - | MAX. |
| $V_{GD}$ | $V_D=V_{DRM} T_j=150$<br>$R_L=3.3K$ | - - | MAX. |



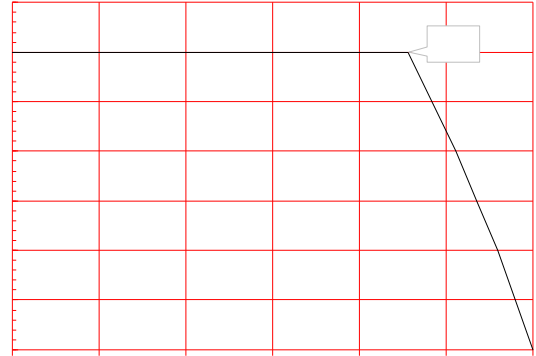




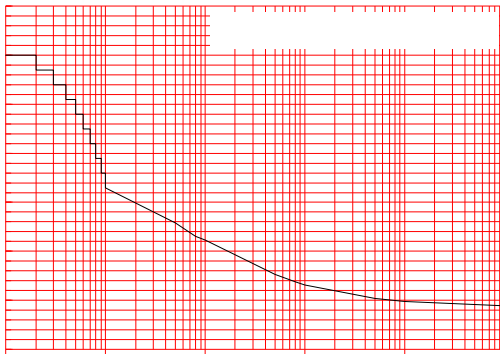
Maximum power dissipation versus RMS on-state current



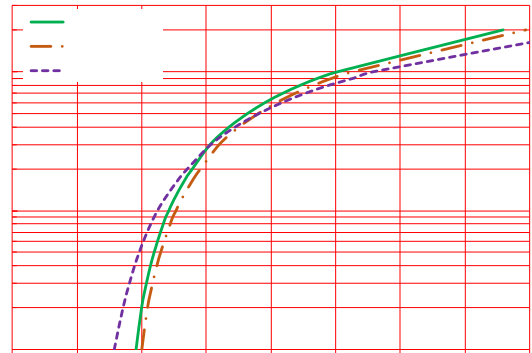
RMS on-state current versus case temperature



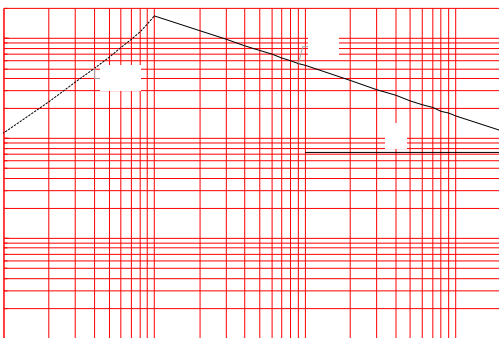
Surge peak on-state current versus number of cycles



On-state characteristics



Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 20\text{ms}$ , and corresponding value of  $I^2t$  ( $dI/dt < 100\text{A}/\mu\text{s}$ )



Relative variations of gate trigger current, holding current and latching current versus junction temperature

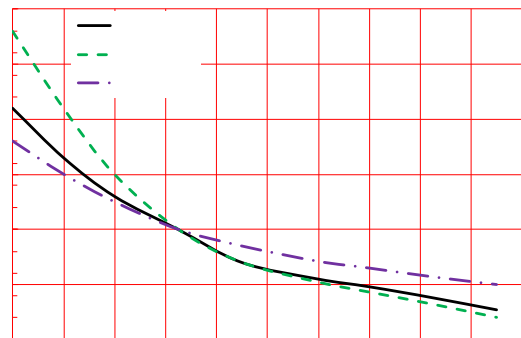
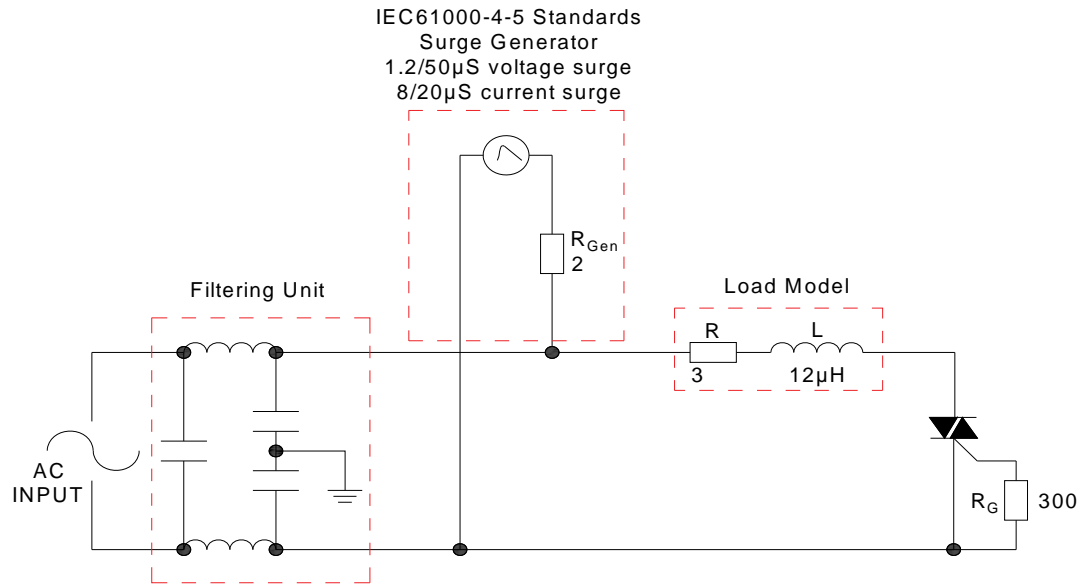




FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



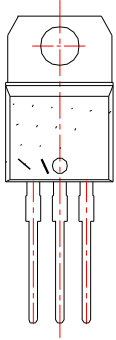
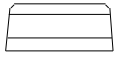
Refer to Instructions for installation of plastic-sealed in-line power devices released by JieJie



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| Date         | Revision | Changes      |
|--------------|----------|--------------|
| Apr.11, 2023 | A.1.0    | Last updated |






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