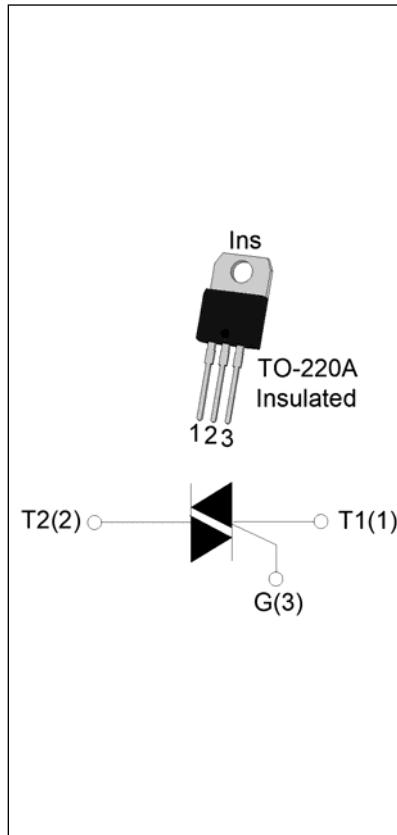


The T0435H-8A triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. Compared to traditional triacs, T0435H-8A provides a very high switching capability up to junction temperatures of 150°C. By using an internal ceramic pad, T0435H-8A provides a rated insulation voltage of 2500 VRMS, complying with UL standards (File ref: E252906). Package TO-220A is RoHS compliant.



Symbol	Value	Unit
$I_{T(RMS)}$	4	A
V_{DRM}/V_{RRM}	800	V
$I_{GT} / /$	35/35/35	mA

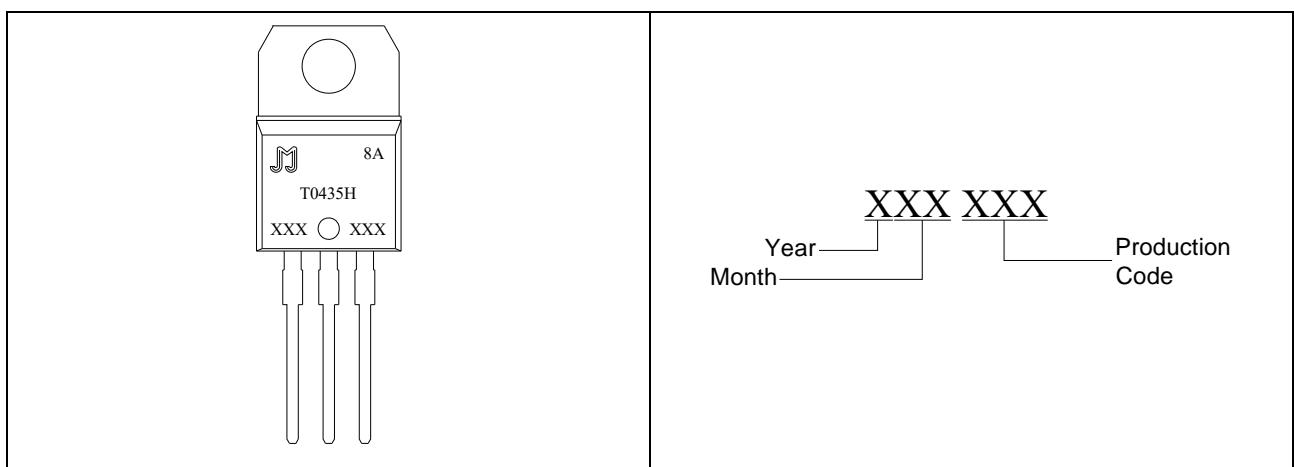
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-150	
Repetitive peak off-state voltage ($T_j=25^\circ C$)	V_{DRM}	800	V
Repetitive peak reverse voltage ($T_j=25^\circ C$)	V_{RRM}	800	V
RMS on-state current ($T_c = 130^\circ C$)	$I_{T(RMS)}$	4	A
Non repetitive surge peak on-state current (full cycle , $t_p=20ms$, $T_j=25^\circ C$)	I_{TSM}	40	A
Non repetitive surge peak on-state current (full cycle , $t_p=16.6ms$, $T_j=25^\circ C$)		44	
I^2t value for fusing ($t_p=10ms$, $T_j=25^\circ C$)	I^2t	8	A^2s
Critical rate of rise of on-state current ($I_G=2 I_{GT}$, $f=100Hz$, $T_j=150^\circ C$)	dI/dt	80	$A/\mu s$
Peak gate current ($t_p=20\mu s$, $T_j=150^\circ C$)	I_{GM}	4	A

Average gate power dissipation ($T_j=150^\circ\text{C}$)P_G(T_j=25°C, non-repetitive)P_GP_GP_GP_GP_G(T_j=25°C unless otherwise specified)

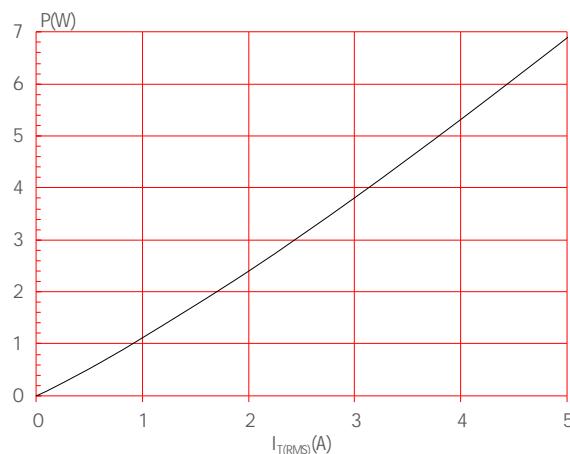
I _{GT}	V _G =10V R _L =20Ω	- -	MAX.	35	mA
V _{GT}		- -	MAX.	1	V
V _{GD}	V _D =V _{DRM} T _j =150°C R _L =3.3K	- -	MIN.	0.2	V



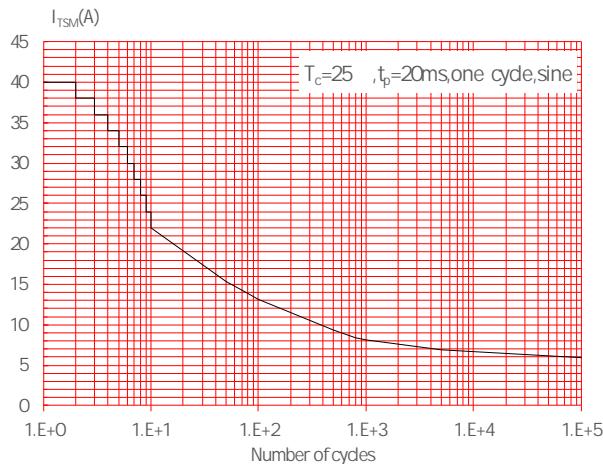
T	04	35	H	-8	A
Triacs					
	<u>$I_{T(RMS)}:4A$</u>				
		<u>35:IGT1-3 35mA</u>			<u>8:V_{DRM} / V_{RRM} 800V</u>
					<u>High junction temperature</u>



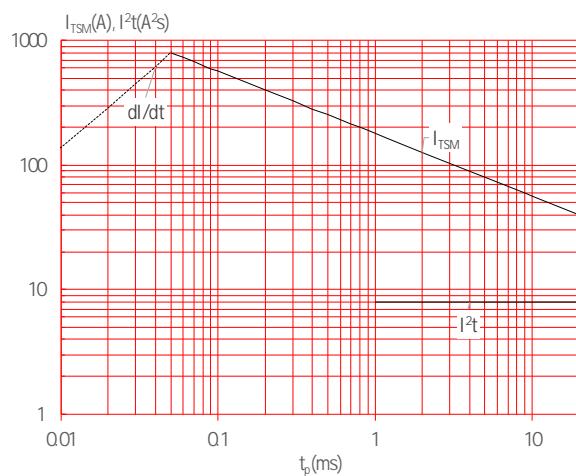
Maximum power dissipation versus RMS on-state current



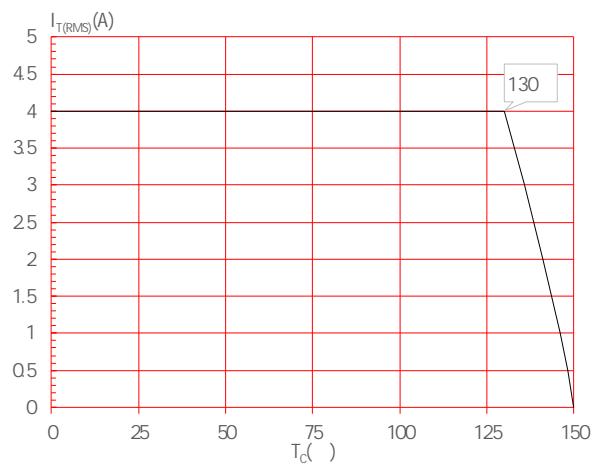
Surge peak on-state current versus number of cycles



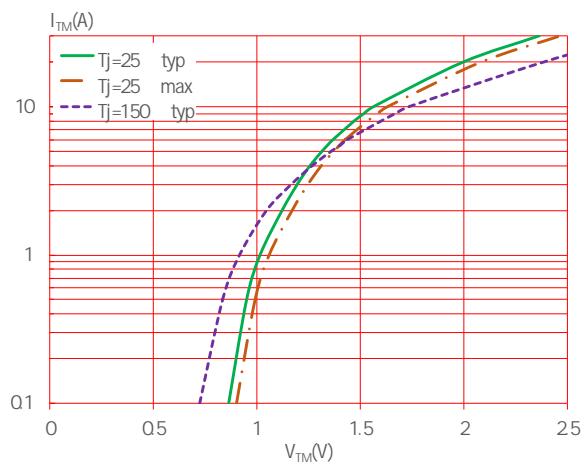
Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$, and corresponding value of I^2t ($dI/dt < 80\text{A}/\mu\text{s}$)



RMS on-state current versus case temperature



On-state characteristics



Relative variations of gate trigger current, holding current and latching current versus junction temperature

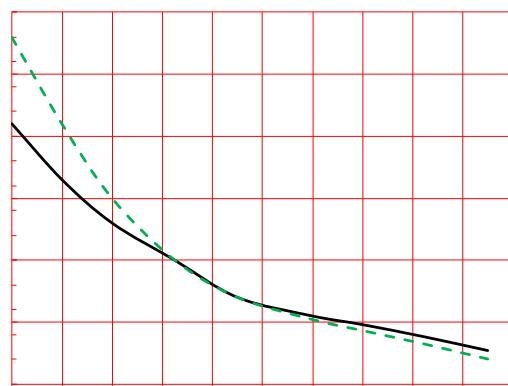
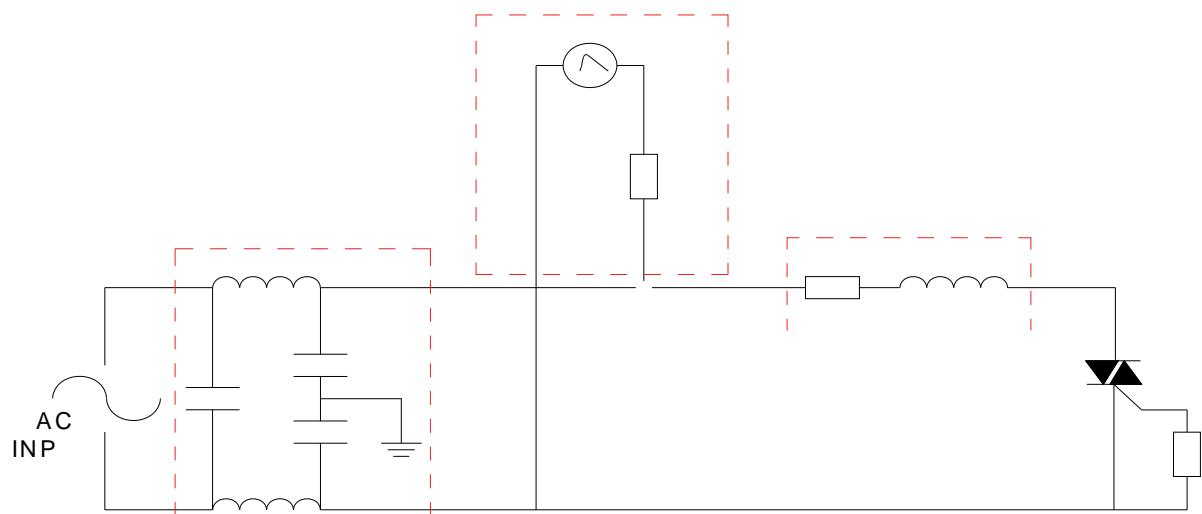


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards





Date	Revision	Changes
Apr.10, 2023	A.1.0	Last updated

