



T0420H-8C 4A TRIAC

Rev.A.1.0

DESCRIPTION:

The T0420H-8C triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. Compared to traditional triacs, T0420H-8C provides a very high switching capability up to junction temperatures of 150°C. From T2 terminals to external heatsink. Package TO-220C is RoHS compliant.

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O



Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.7)	V_{pp}	4	kV
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ELECTRICAL CHARACTERISTICS ($T_j=25$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V$ $R_L=33$	- -	MAX.	20	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=150$ $R_L=3.3K$	- -	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	-	MAX.	30	mA
				40	
I_H	$I_T=100mA$		MAX.	25	mA
dV/dt	$V_D=540V$ Gate Open $T_j=150$		MIN.	800	V/ μs
$(dI/dt)_c$	$(dV/dt)_c=20V/\mu s$, $T_j=150$		MIN.	5	A/ms
t_{on}	$I_G=40mA$ $I_A=200mA$ $I_R=20mA$ $T_j=25$		TYP.	3	μs
t_{off}				30	

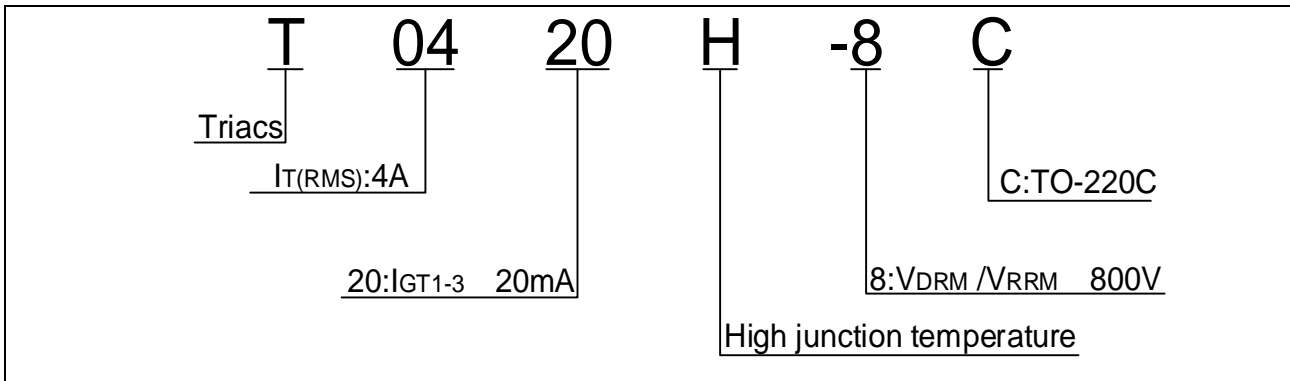
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=5.5A$ $t_p=380\mu s$	$T_j=25$	1.4	V
V_{TO}	Threshold voltage	$T_j=150$	0.6	V
R_D	Dynamic resistance	$T_j=150$	129	m
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25$	5	μA
I_{RRM}		$T_j=150$	1	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	2	$^{\circ}W$
$R_{th(j-a)}$	junction to ambient (AC)	60	$^{\circ}W$

ORDERING INFORMATION



MARKING

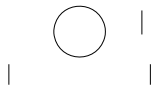


FIG.1 Maximum power dissipation versus RMS on-state current

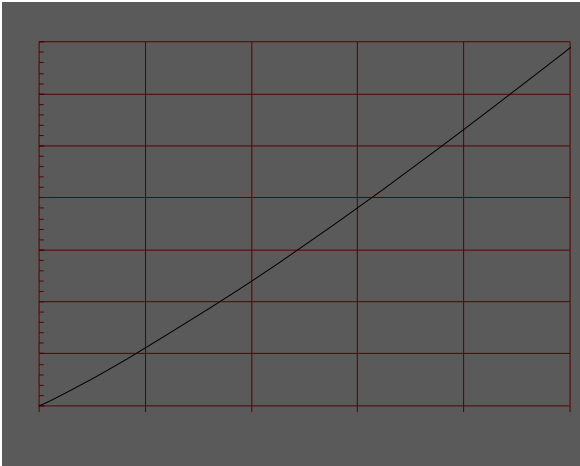


FIG.2: RMS on-state current versus case temperature

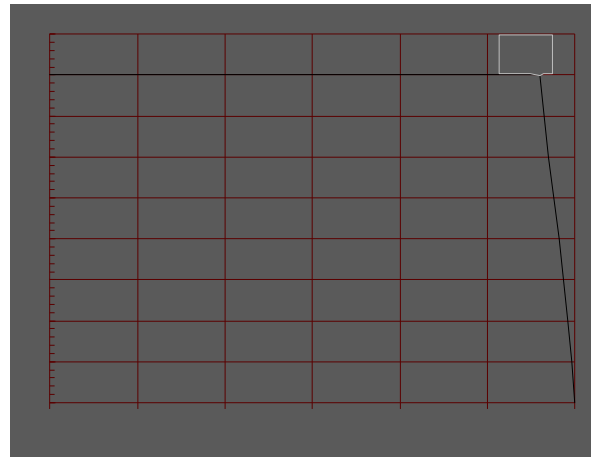


FIG.3: Surge peak on-state current versus number of cycles

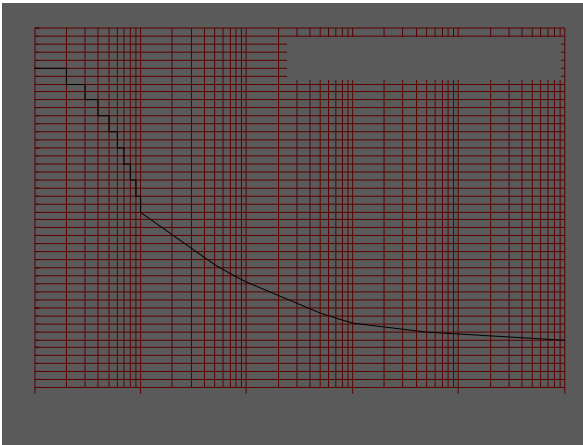


FIG.4: On-state characteristics

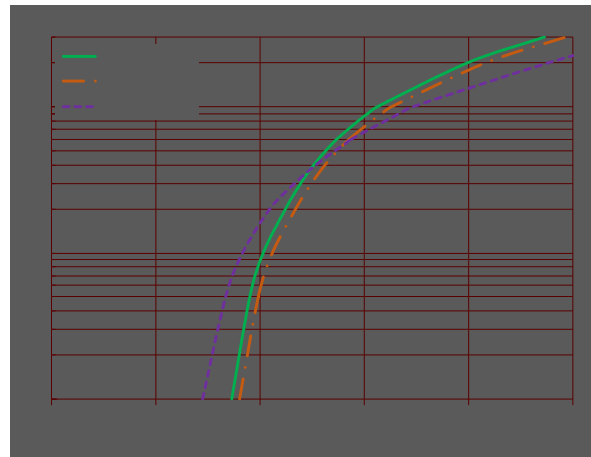


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$, and corresponding value of I^2t ($di/dt < 80\text{A}/\mu\text{s}$)

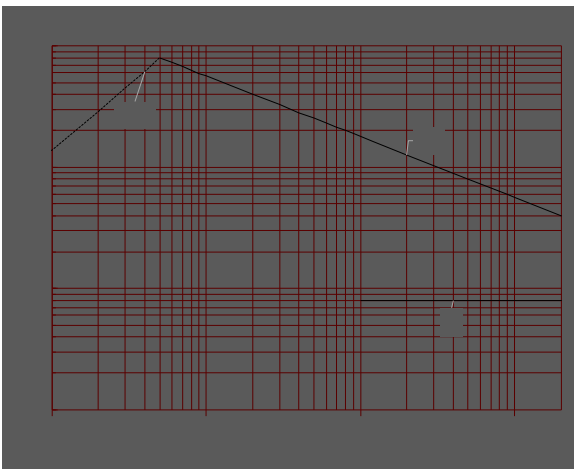


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

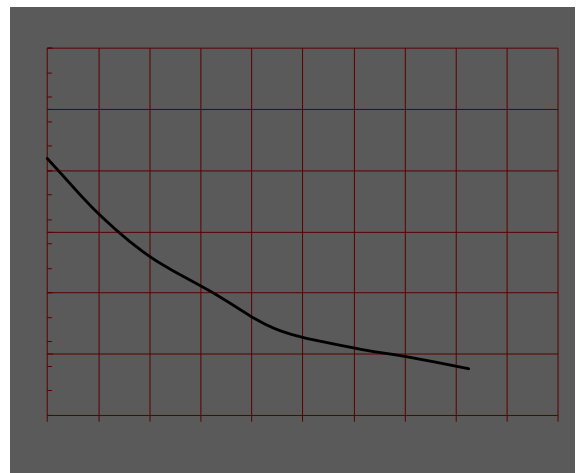
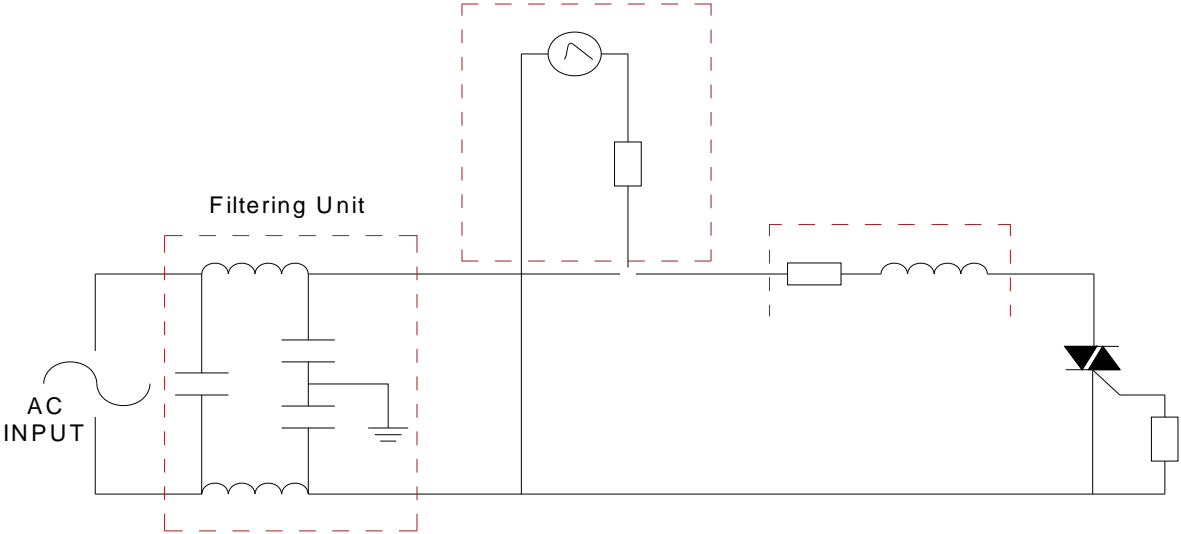


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards





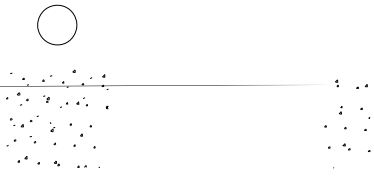
ORDERING INFORMATION

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
T0420H-8C	800	20	TO-220C	50	Tube

Document Revision History

Date	Revision	Changes
Apr.10, 2023	A.1.0	Last updated

PACKAGE MECHANICAL DATA





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