

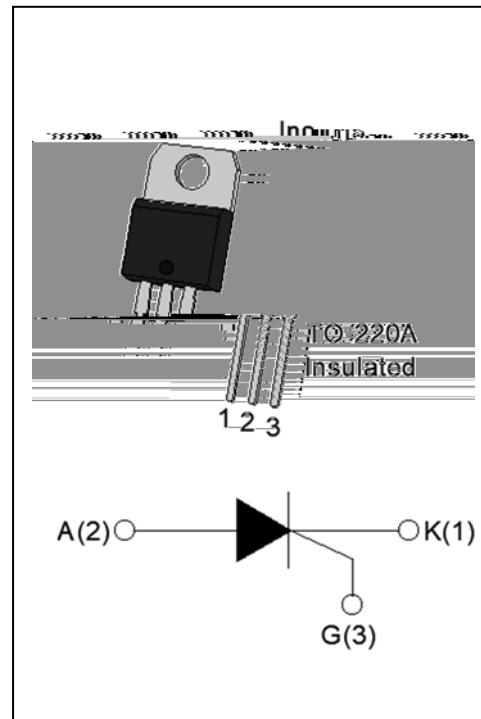


JX080A 8A Sensitive SCR

Rev.A.1.0

DESCRIPTION:

The JX080A SCR provides high dV/dt rate with strong resistance to electromagnetic interface. It is especially recommended for use on residual current circuit breaker, straight hair, igniter etc. By using an internal ceramic pad, JX080A provides a rated insulation voltage of 2500 VRMS,complying with UL standards (File ref: E252906). Package TO-220A is RoHS compliant.



MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	8	A
V_{DRM} / V_{RRM}	800	V
I_{GT}	200	μA

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-110	
Repetitive peak off-state voltage ($T_j=25^\circ C$)	V_{DRM}	800	V
Repetitive peak reverse voltage ($T_j=25^\circ C$)	V_{RRM}	800	V
Average on-state current ($T_c = 82^\circ C$)	$I_{T(AV)}$	5	A
RMS on-state current ($T_c = 82^\circ C$)	$I_{T(RMS)}$	8	A
Non repetitive surge peak on-state current ($t_p=10ms, T_j=25^\circ C$)	I_{TSM}	80	A
Non repetitive surge peak on-state current ($t_p=8.3ms, T_j=25^\circ C$)		88	
I^2t value for fusing ($t_p=10ms, T_j=25^\circ C$)	I^2t	32	A^2s
Critical rate of rise of on-state current ($I_G=2 \times I_{GT}, f=100Hz, T_j=110^\circ C$)	di/dt	100	$A/\mu s$
Peak gate current ($t_p=20\mu s, T_j=110^\circ C$)	I_{GM}	4	A

Average gate power dissipation ($T_j=110^\circ C$)	$P_{G(AV)}$	1	W
Peak gate power	P_{GM}	10	W
Peak pulse voltage ($T_j=25^\circ C$; non-repetitive, off-state; FIG.7)	V_{pp}	0.5	kV

ELECTRICAL CHARACTERISTICS

unless otherwise specified

Symbol	Test Condition	Value			Unit
		MIN.	TYP.	MAX.	
I_{GT}	$V_D=12V R_L=33$	-	60	200	μA
V_{GT}		-	-	0.8	V
V_{GD}	$V_D=V_{DRM} T_j=110^\circ C$	0.2	-	-	V
I_L	$I_G=1.2 I_{GT}$	-	-	6	mA
I_H	$I_T=0.05A$	-	-	5	mA
dV/dt	$V_D=540V T_j=110^\circ C R_{GK}=1K$	50	-	-	V/ μs
	$V_D=540V T_j=110^\circ C R_{GK}=220$	200	-	-	
t_{on}	$I_G=10mA I_A=20mA I_R=2mA T_j=25^\circ C$	-	2	-	μs
t_{off}		-	70	-	μs

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_T=16A t_p=380\mu s$	$T_j=25^\circ C$	1.6	V
V_{TO}	Threshold voltage	$T_j=110^\circ C$	0.79	V
R_D	Dynamic Resistance	$T_j=110^\circ C$	0.04	
I_{DRM}	$V_D=V_{DRM} V_R=V_{RRM}$		5	μ

ORDERING INFORMATION

<u>J</u>	<u>X</u>	<u>080</u>	<u>A</u>
<u>JieJie Microelectronics Co., Ltd.</u>			
	<u>Sensitive gate SCRs</u>		
		<u>I_{T(RMS)}:8A</u>	<u>A:TO-220A(Ins)</u>

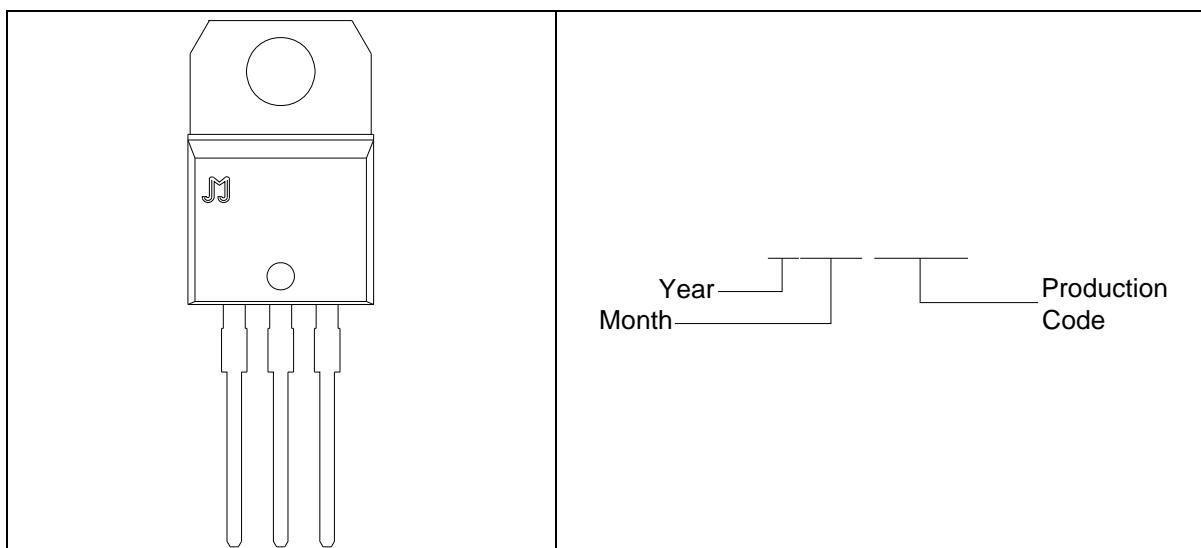
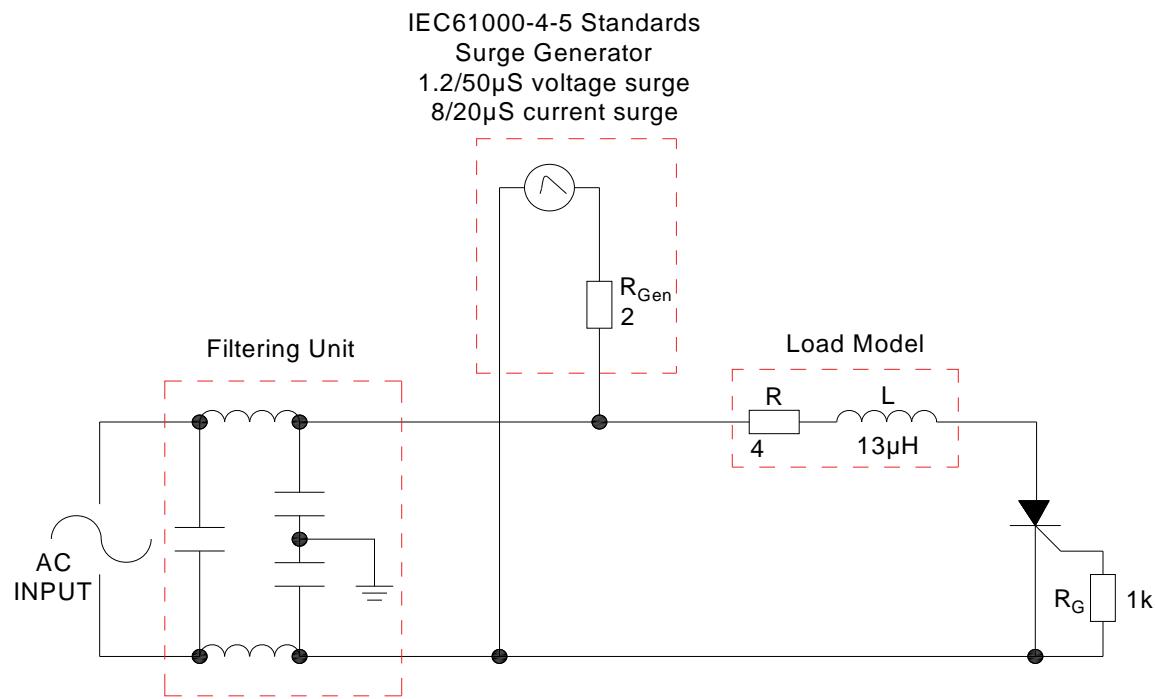
MARKING

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards.



SHAPING AND SOLDERING PARAMETERS

Refer to Instructions for installation of plastic-sealed in-line power devices released by JieJie

ORDERING INFORMATION

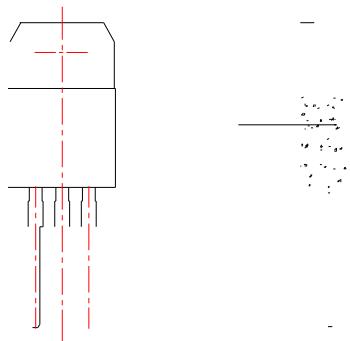
Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(μA)	Package	Base qty. (pcs)	Delivery mode
JX080A	800	200	TO-220A(Ins)	50	Tube

Document Revision History

Date	Revision	Changes
May.18, 2023	A.1.0	Last update



PACKAGE MECHANICAL DATA





Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it.

Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents.

Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.

is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.

Copyright © 2023 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.