JCT825C



Peak gate power P_{GM} 20 W

Peak pulse voltage

(T_j=25 ; non-repetitive,off-state;FIG.7)

TEL +86-513-68528666 http://www.jjwdz.com

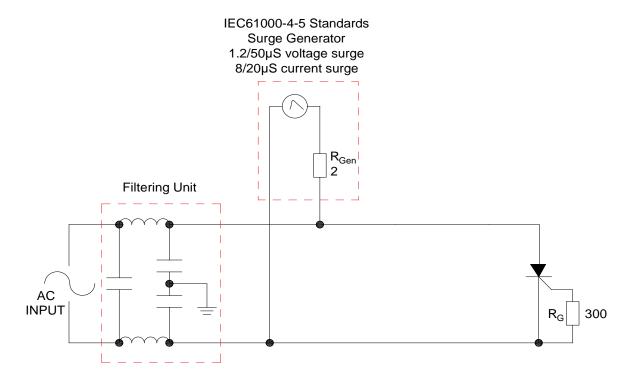
JCT825C



FIG.1 Maximum power dissipation versus RMS on-state current	FIG.2: RMS of temperature	

FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards.



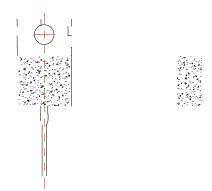
Refer to Instructions for installation of plastic-sealed in-line power devices released by JieJie

Order code	Voltage V _{DRM} /V _{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
JCT825C	800	20	TO-220C	50	Tube

Document Revision History

Date	Revision	Changes
Apr.13, 2023	A.1.0	Last update

TEL +86-513-68528666 http://www.jjwdz.com



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement. Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.

is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd. Copyright © 2023 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.