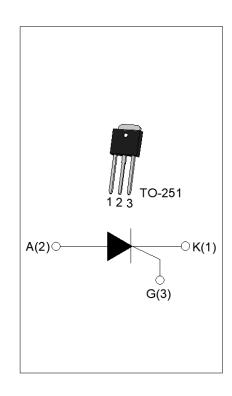


JCT812H 12A SCR

Rev.A.1.0

With high ability to withstand the shock loading of large current, JCT812H of silicon controlled rectifiers provides high dV/dt rate with strong resistance to electromagnetic interference. It is especially recommended for use on solid state relay, motorcycle, power charger, T-tools etc. Package TO-251 is RoHS compliant.

Symbol	Value	Unit
I _{T(RMS)}	12	А
V _{DRM} /V _{RRM}	800	٧
lgт	15	mA



Parameter	Symbol	Value	Unit
Storage junction temperature range	T _{stg}	-40-150	
Operating junction temperature range	Tj	-40-125	
Repetitive peak off-state voltage (T _j =25)	V _{DRM}	800	V
Repetitive peak reverse voltage (T _j =25)	V _{RRM}	800	V
Average on-state current (Tc 63)	I _{T(AV)}	7.6	Α
RMS on-state current (T _C 63)	I _{T(RMS)}	12	Α
Non repetitive surge peak on-state current (t _p =10ms, T _j =25)	I	140	۸
Non repetitive surge peak on-state current $(t_p=8.3ms, T_j=25)$	Ттѕм	154	A
I ² t value for fusing (t _p =10ms , T _j =25)	l ² t	98	A ² s
Critical rate of rise of on-state current (I_{G} =2 I_{GT} , f=100Hz , T_{j} =125)	dl/dt	150	A/µs
Peak gate current (t _p =20µs , T _j =125)	I _{GM}	4	Α

JCT812H

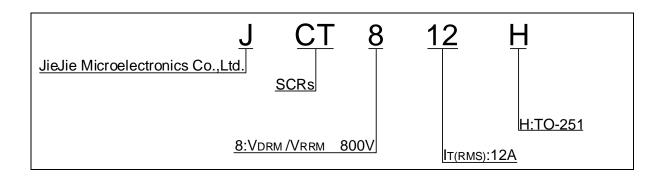


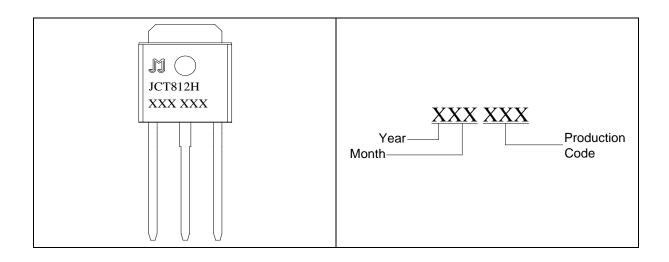
Average gate power dissipation (T _j =125)	P _{G(AV)}	1	W
Peak gate power	P _{GM}	10	W

٧

Peak pulse voltage

 $(T_j=25$; non-repetitive, off-state; FIG.7)





JCT812H

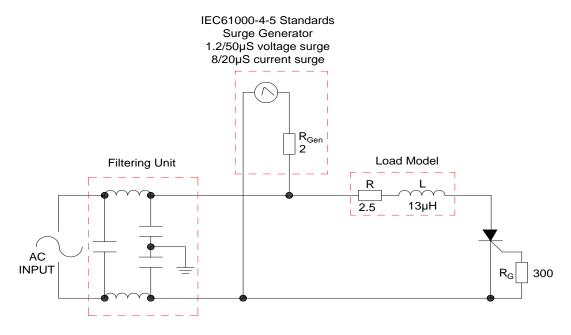


FIG.1 Maximum power dissipation versus RMS on-state current



FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards.



Refer to Instructions for installation of plastic-sealed in-line power devices released by JieJie

Order code	Voltage V _{DRM} /V _{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
JCT812H	800	15	TO-251	80	Tube

Document Revision History

Date	Revision	Changes
Apr.13, 2023	A.1.0	Last update

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