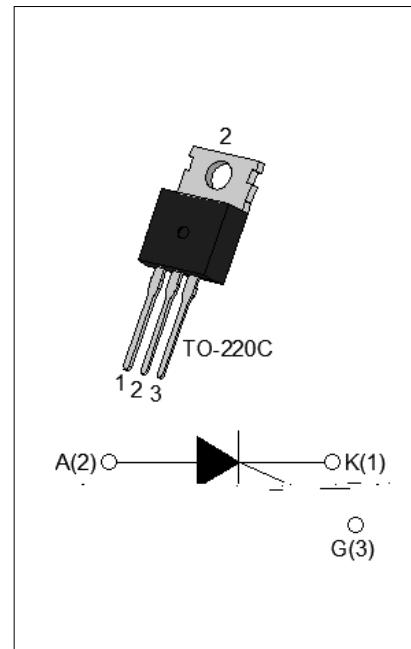




With high ability to withstand the shock loading of large current, JCT625C SCR provides high dV/dt rate with strong resistance to electromagnetic interference. It is especially recommended for use on solid state relay, motorcycle, power charger, T-tools etc. Package TO-220C is RoHS compliant.

Symbol	Value	Unit
$I_{T(RMS)}$	25	A
V_{DRM}/V_{RRM}	600	V
I_{GT}	20	mA



Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage ($T_j=25^\circ C$)	V_{DRM}	600	V
Repetitive peak reverse voltage ($T_j=25^\circ C$)	V_{RRM}	600	V
Average on-state current ($T_c = 102^\circ C$)	$I_{T(AV)}$	16	A
RMS on-state current ($T_c = 102^\circ C$)	$I_{T(RMS)}$	25	A
Non repetitive surge peak on-state current ($t_p=10ms, T_j=25^\circ C$)	I_{TSM}	320	A
Non repetitive surge peak on-state current ($t_p=8.3ms, T_j=25^\circ C$)		352	
I^2t value for fusing ($t_p=10ms, T_j=25^\circ C$)	I^2t	512	A^2s
Critical rate of rise of on-state current ($I_G=2 \times I_{GT}, f=100Hz, T_j=125^\circ C$)	dI/dt	200	A/s
Peak gate current ($t_p=20\mu s, T_j=125^\circ C$)	I_{GM}	5	A
Average gate power dissipation ($T_j=125^\circ C$)	$P_{G(AV)}$	1	W

Peak gate power	P_{GM}	20	W
Peak pulse voltage ($T_j=25^\circ C$; non-repetitive, off-state; FIG.7)	V_{PP}	0.5	kV

(T_j=25 °C unless otherwise specified)

Symbol	Test Condition	Value			Unit
		MIN.	TYP.	MAX.	
I _{GT}	V _D =12V R _L =33	-	-	20	mA
V _{GT}		-	-	1	V
V _{GD}	V _D =V _{DRM} T _j =125 °C R _L =3.3K	0.2	-	-	V
I _L	I _G =1.2I _{GT}	-	-	70	mA
I _H	I _T =500mA	-	-	60	mA
dV/dt	V _D =400V Gate Open T _j =125 °C	1200	-	-	V/s
t _{on}	I _G =20mA I _A =200mA I _R =20mA T _j =25 °C	-	2	-	s
t _{off}		-	50	-	

Symbol	Parameter	Value(MAX.)	Unit
V _{TM}	I _{TM} =50A t _p =380 s	1.5	V
V _{TO}	Threshold voltage	0.72	V
R _D	Dynamic resistance	16	
I _{DRM}	V _D =V _{DRM} V _R =V _{RRM}	5	A
I _{RRM}		0.5	mA

Symbol	Parameter	Value	Unit
R _{th(j-c)}	junction to case(DC)	0.7	/W
R _{th(j-a)}	junction to ambient (DC)	43	/W

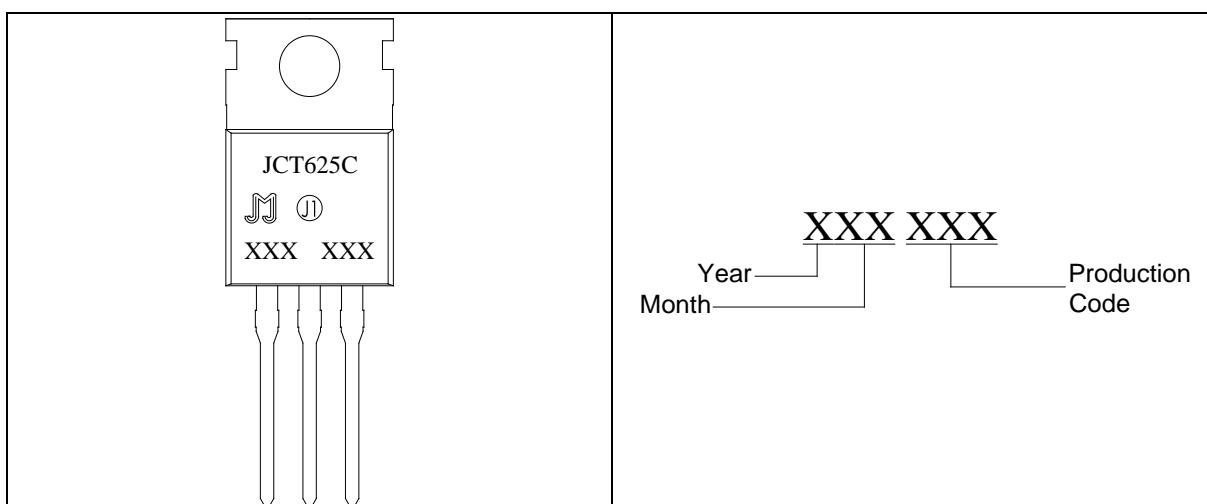
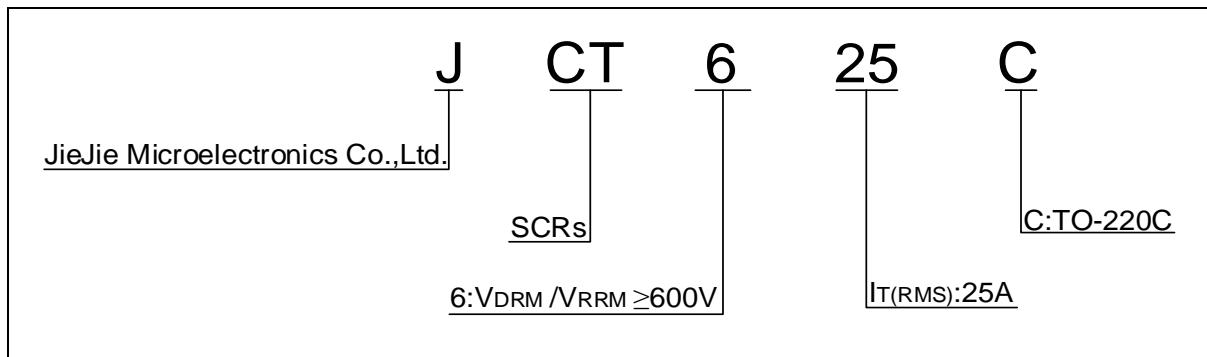


FIG.1 Maximum power dissipation versus RMS on-state current

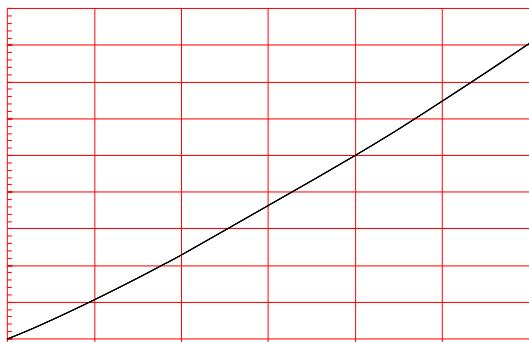
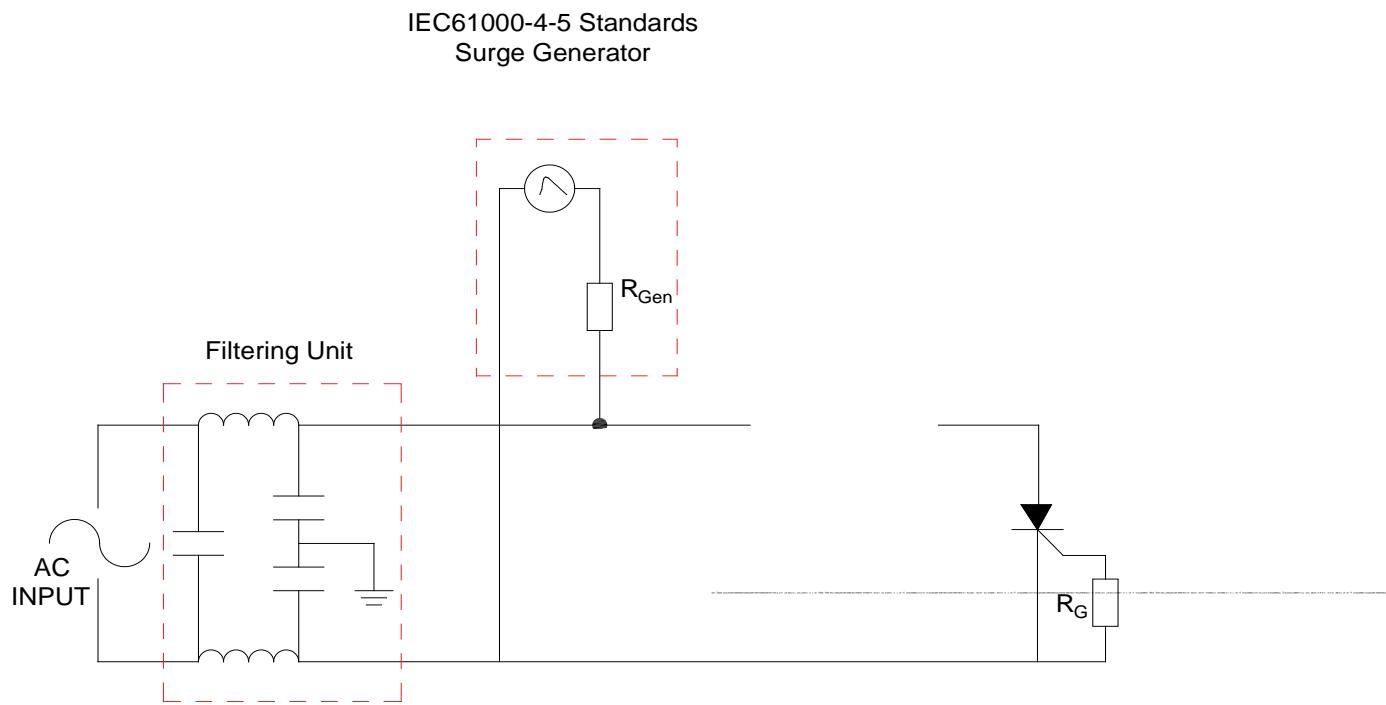


FIG.2: RMS on-state current versus case temperature

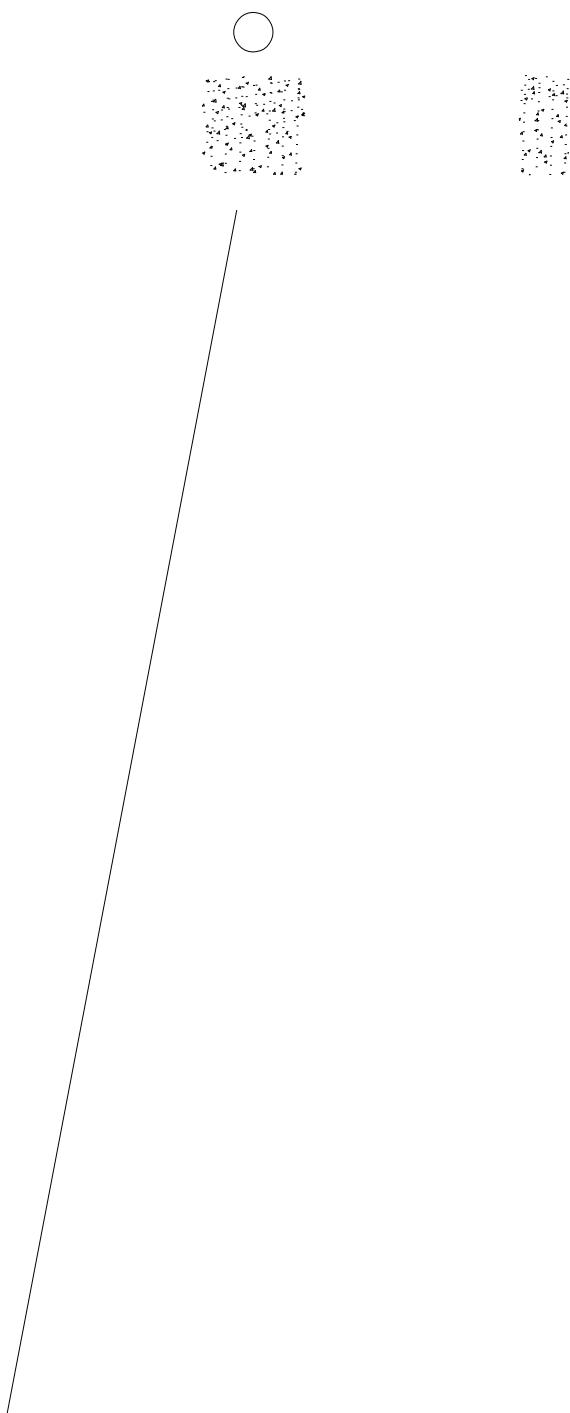
FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards.



Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
JCT625C	600	20	TO-220C	50	Tube

Document Revision History

Date	Revision	Changes
Apr.13, 2023	A.1.0	Last update



Information furnished in this document is believed to be accurate and reliable.

However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for any errors or omissions.