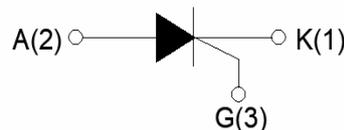
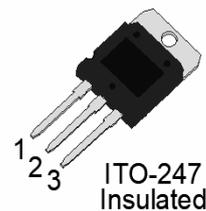




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Kit \ \ ig \ ability to kit \ stand t \ e s \ ock loading o Z large current, JCT16130IS SCR provides \ ig \ dV/dt rate kit \ strong resistance to electromagnetic inter Z erence. It is especially recommended Z or use on solid state relay, UPS, SVC, po k er c \ arger, T-tools etc. From all t \ ree terminals to e l ternal \ eatsink, JCT16130IS provides a rated insulation voltage o Z 2500V, Package ITO-247 is RoHS compliant.



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Symbol	Value	Unit
$I_{T(RMS)}$	130	A
$V_{DRM}/V_{RRM}$	1600	V
$I_{GT}$	20-80	mA

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Parameter	Symbol	Value	Unit
Storage ^ unctio n temperature range	$T_{tg}$	-40-150	
Operating ^ unctio n temperature range	$T$	-40-125	
Repetitive peak o Z Z-state voltage (T 25 )	$V_{DRM}$	1600	V
Repetitive peak reverse voltage (T 25 )	$V_{RRM}$	1600	V
Average on-state current (T 46 )	$I_{T(AV)}$	83	A
RMS on-state current (T 46 )	$I_{T(RMS)}$	130	A
Non repetitive surge peak on-state current (tp 10ms , T1 25 )	$I_{TSM}$	1500	A
Non repetitive surge peak on-state current (tp 1 8.3ms , T1 25 )		1650	
I²t value Z or Z usin g (tp 10ms , T1 25 )	$I^2t$	11250	A²s
Critical rate o Z rise Z on-state current (Ig 1 2y IGT , Z 1 100H n 1 T25 )	$di/dt$	250	A/ s

Peak gate current ( $t \leq 20 \mu s, T = 125^\circ C$ )	$I_{GM}$	15	A
Average gate power dissipation ( $T = 125^\circ C$ )	$P_{G(AV)}$	1	K
Peak gate power	$P_{GM}$	25	K
Peak pulse voltage ( $T = 125^\circ C$ / non-repetitive, $Z-Z$ -state / FIG.7)	$V_{pp}$	1.3	kV

Symbol / Unit / Test Condition / Value (MIN. / TYP. / MAX.)

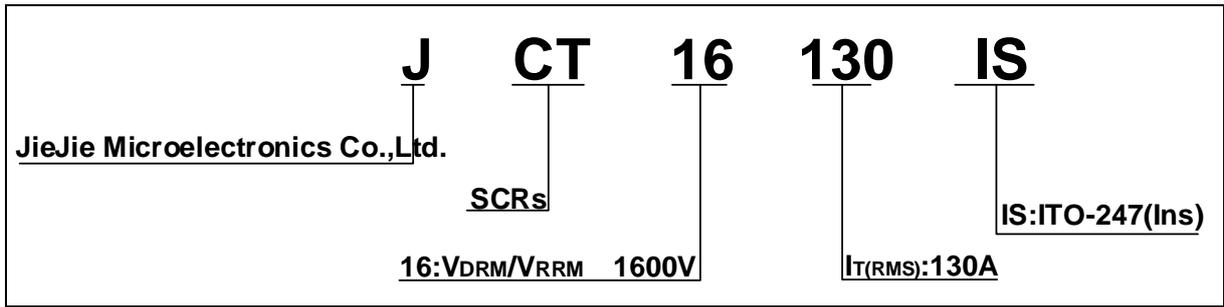
Symbol	Test Condition	Value			Unit
		MIN.	TYP.	MAX.	
$I_{GT}$	$V_D = 12V, R_L = 3.3K$	20	-	80	mA
$V_{GT}$		-	-	1.3	V
$V_{GD}$	$V_D = 100V, T = 125^\circ C, R_L = 3.3K$	0.25	-	-	V
$I_L$	$I_G = 1.2A$	-	-	280	mA
$I_H$	$I_T = 1A$	-	-	250	mA
dV/dt	$V_D = 1070V$ Gate Open $T = 125^\circ C$	2000	-	-	V/ $\mu s$
$t_{on}$	$I_G = 100mA, I_T = 1A, I_D = 100mA$ $T = 125^\circ C$	-	7	-	s
$t_{off}$		-	200	-	

Symbol / Unit / Parameter / Value (MAX.)

Symbol	Parameter	Value (MAX.)	Unit
$V_{TM}$	$I_{TM} = 180A, t = 380 \mu s, T = 125^\circ C$	1.6	V
$V_{TO}$	Turn-off voltage	0.86	V
$R_D$	Dynamic resistance	4.5	m
$I_{DRM}$	$V_D = 100V, V_R = 100V$	$T = 125^\circ C$	20

Symbol	Parameter	Value	Unit
$R_{t(j-c)}$ <td>junction to case (DC)</td> <td>0.4</td> <td>/K</td>	junction to case (DC)	0.4	/K
$R_{t(j-a)}$ <td>junction to ambient (DC)</td> <td>50</td> <td>/K</td>	junction to ambient (DC)	50	/K

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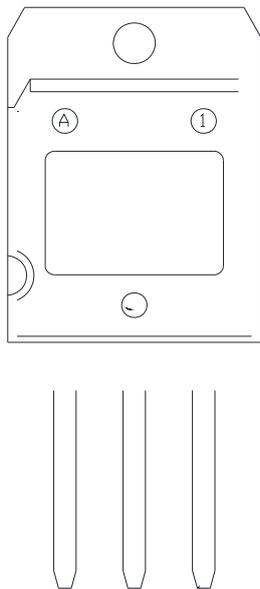


FIG.1 Maximum power dissipation versus RMS on-state current

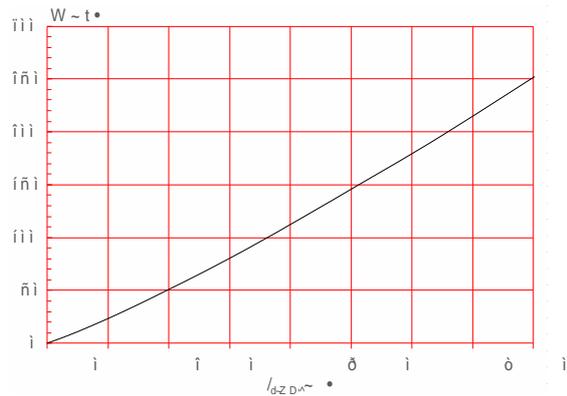


FIG.2: RMS on-state current versus case temperature

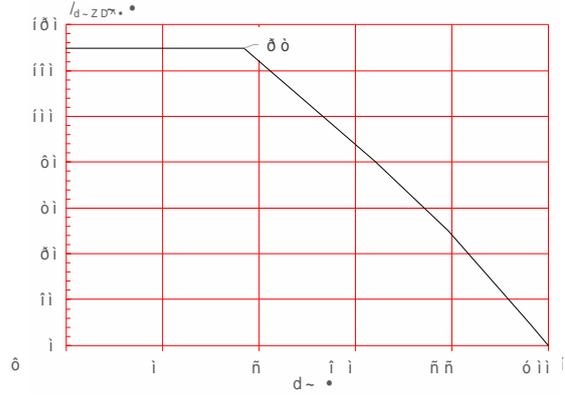


FIG.3: Surge peak on-state current versus number of Z cycles

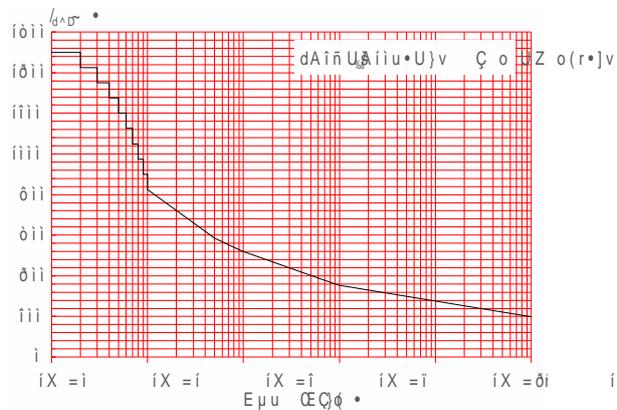


FIG.4: On-state characteristics

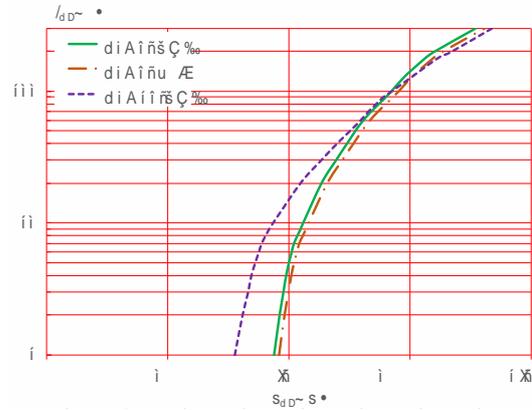


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with  $t_p \le 20ms$ , and corresponding value  $dI/dt$  ( $dI/dt \le 250A/\mu s$ )

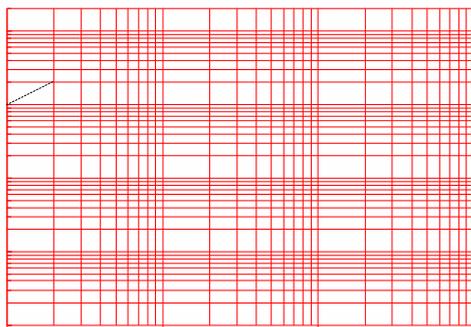
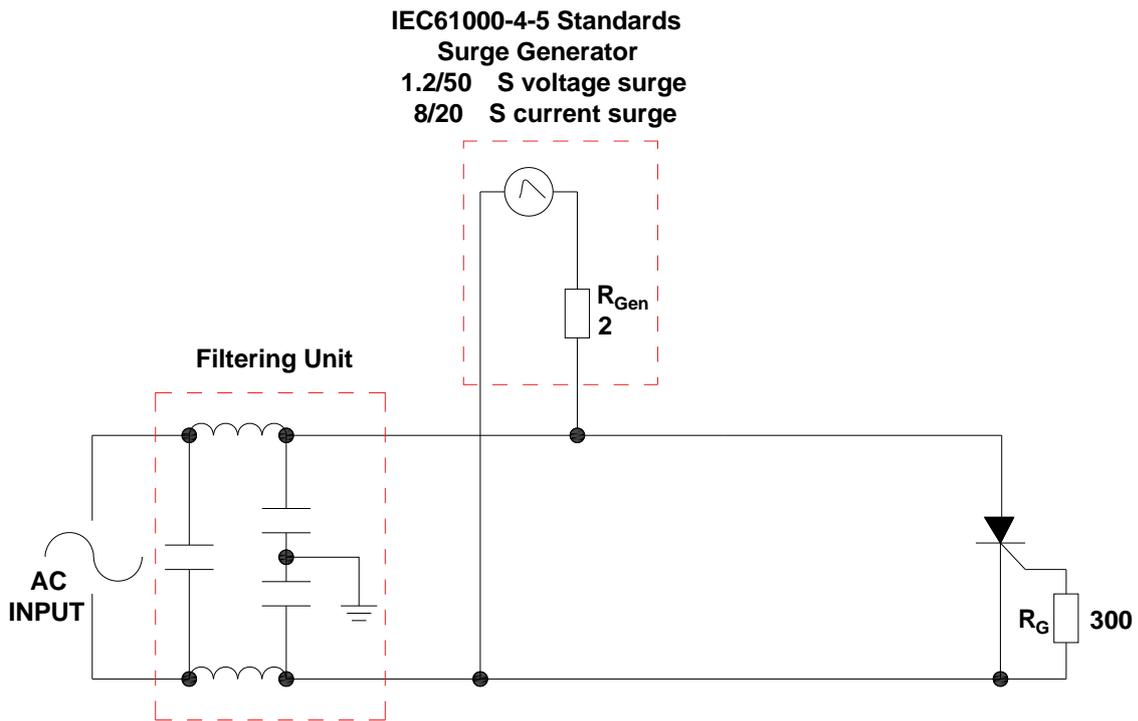


FIG.7 Test circuit Z or inductive and resistive loads to IEC-61000-4-5 standards.



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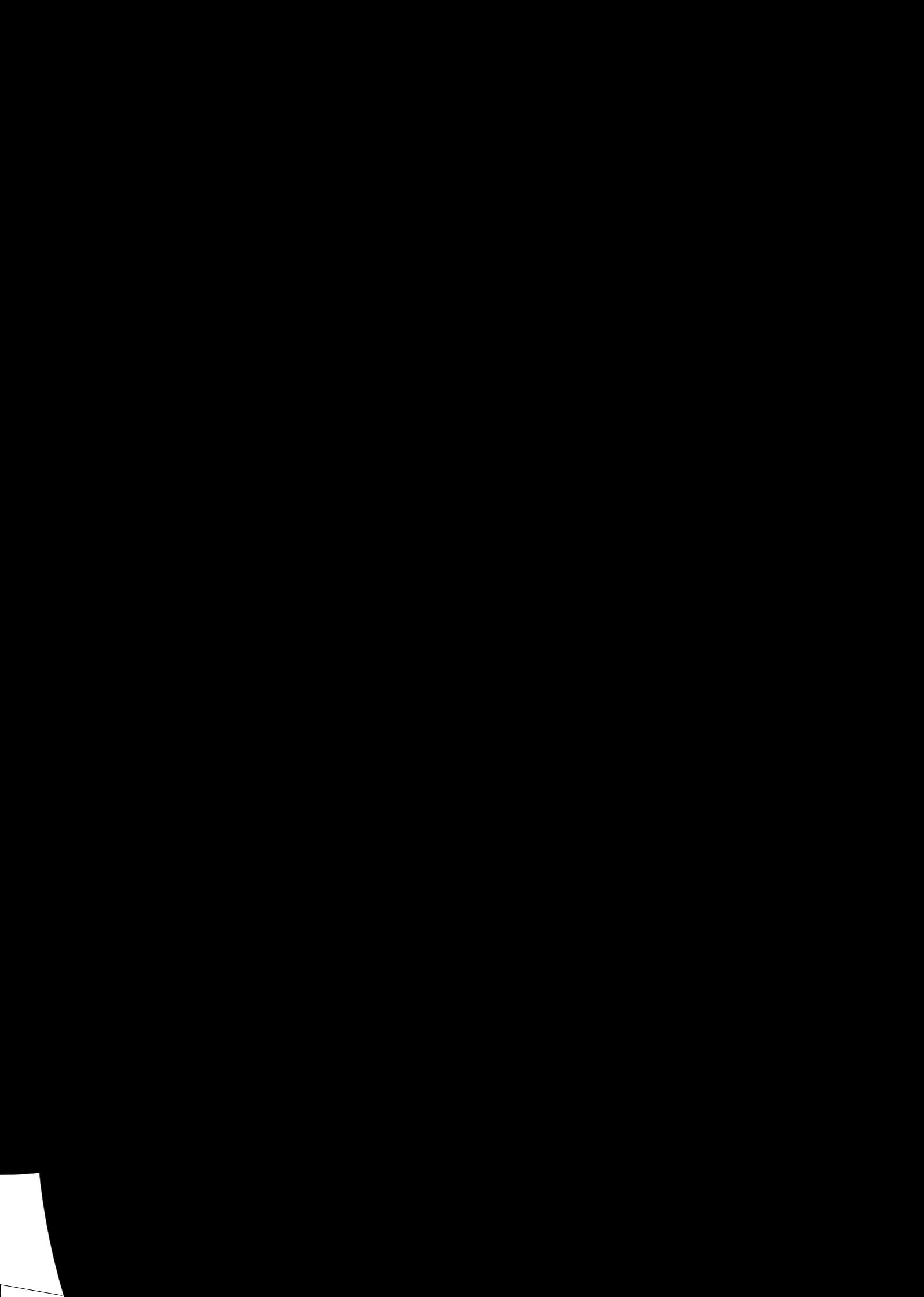
Re Z er to Instructions Z or installation in plastic-sealed in-line po k erides released by JieJie

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Order code	Voltage V <sub>DRM</sub> /V <sub>RRM</sub> (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
JCT16130IS	1600	20-80	ITO-247(Ins)	25	Tube

**Document Revision History**

Date	Revision	C \ anges
Apr.13, 2023	A.1.0	Last update
Feb.1 - , 2024	A.1.1	Rene k R <sub>t</sub> \ (^ -ø) V <sub>TM</sub>



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