

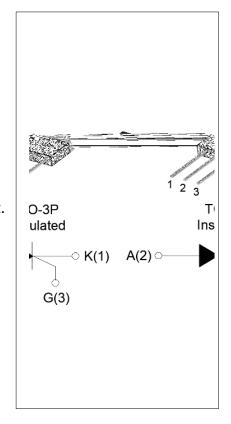


### **JCT1255Z 55A SCR**

Rev.A.1.0

#### **DESCRIPTION:**

With high ability to withstand the shock loading of large current, JCT1255Z SCR provides high dV/dt rate with strong resistance to electromagnetic interference. It is especially recommended for use on solid state relay, motorcycle, power charger, T-tools etc. From all three terminals to external heatsink, JCT1255Z provides a rated insulation voltage of 2500 V<sub>RMS</sub>, complying with UL standards (File ref: E252906). Package TO-3P is RoHS compliant.



#### **MAIN FEATURES**

Symbol	Value	Unit
I <sub>T(RMS)</sub>	55	А
V <sub>DRM</sub> /V <sub>RRM</sub>	1200	V
I <sub>GT</sub>	50	mA

### **ABSOLUTE MAXIMUM RATINGS**

Parameter	Symbol	Value	Unit
Storage junction temperature range	T <sub>stg</sub>	-40-150	
Operating junction temperature range	Tj	-40-125	
Repetitive peak off-state voltage (T <sub>j</sub> =25 )	V <sub>DRM</sub>	1200	V
Repetitive peak reverse voltage (T <sub>j</sub> =25 )	V <sub>RRM</sub>	1200	V
Average on-state current (Tc 60 )	I <sub>T(AV)</sub>	35	Α
RMS on-state current (T <sub>C</sub> 60 )	I <sub>T(RMS)</sub>	55	Α
Non repetitive surge peak on-state current (t <sub>p</sub> =10ms, T <sub>j</sub> =25 )	1	700	А
Non repetitive surge peak on-state current $(t_p=8.3ms, T_j=25)$	750		
$I^2$ t value for fusing ( $t_p$ =10ms , $T_j$ =25 )	l <sup>2</sup> t	2450	A <sup>2</sup> s
Critical rate of rise of on-state current (I <sub>G</sub> =2 I <sub>GT</sub> , f=100Hz , T <sub>j</sub> =125 )	dI/dt	200	A/µs

## JCT1255Z

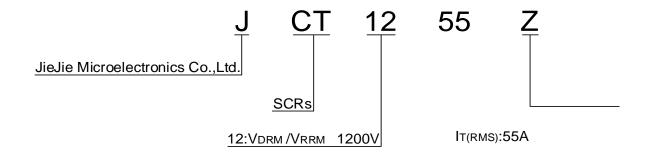
Peak gate current (t <sub>p</sub> =20µs , T <sub>j</sub> =125 )	l <sub>GM</sub>	10	Α
Average gate power dissipation (T <sub>j</sub> =125 )	P <sub>G(AV)</sub>	1	W
Peak gate power	P <sub>GM</sub>	20	W
Peak pulse voltage (T <sub>j</sub> =25 ; non-repetitive,off-state;FIG.7)	V <sub>pp</sub>	0.7	kV

## **ELECTRICAL CHARACTERISTICS** (T<sub>j</sub>=25 unless otherwise specified)

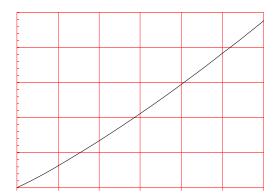
Symbol Test Condition		Value			l lnit
Symbol	rest Condition		TYP.	MAX.	Unit
I <sub>GT</sub>	V <sub>D</sub> =12V R <sub>L</sub> =33	-	-	50	mA
V <sub>G</sub> T	VD=12V KL=33	-	-	1	V
V <sub>GD</sub>	V <sub>D</sub> =V <sub>DRM</sub> T <sub>j</sub> =125 R <sub>L</sub> =3.3K	0.2	-	-	V

IL I<sub>G</sub>=1.2I<sub>GT</sub> - - 120 mA I

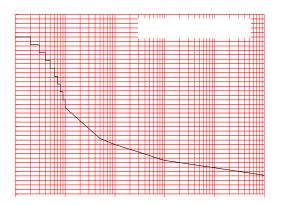
## **ORDERING INFORMATION**



**FIG.1** Maximum power dissipation versus RMS on-state current



**FIG.3:** Surge peak on-state current versus number of cycles



**FIG.2:** RMS on-state current versus case temperature

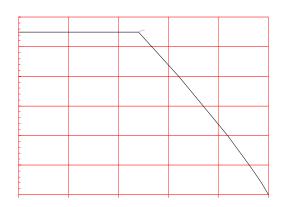


FIG.4: On-state characteristics

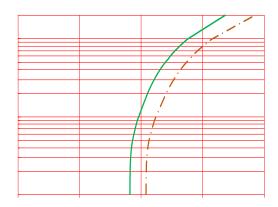
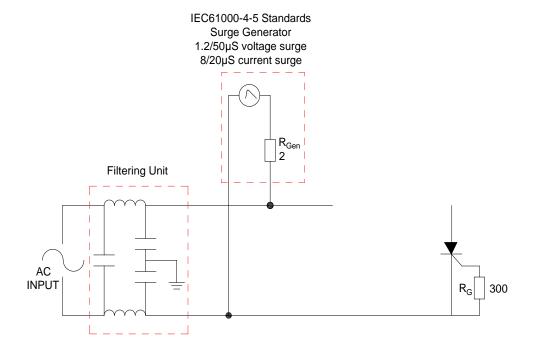


FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards.





## ORDERING INFORMATION

Order code	Voltage V <sub>DRM</sub> /V <sub>RRM</sub> (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
JCT1255Z	1200	50	TO-3P(Ins)	30	Tube

# **Document Revision History**

Date	Revision	Changes
Apr.13, 2023	A.1.0	Last update

# JCT1255Z

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