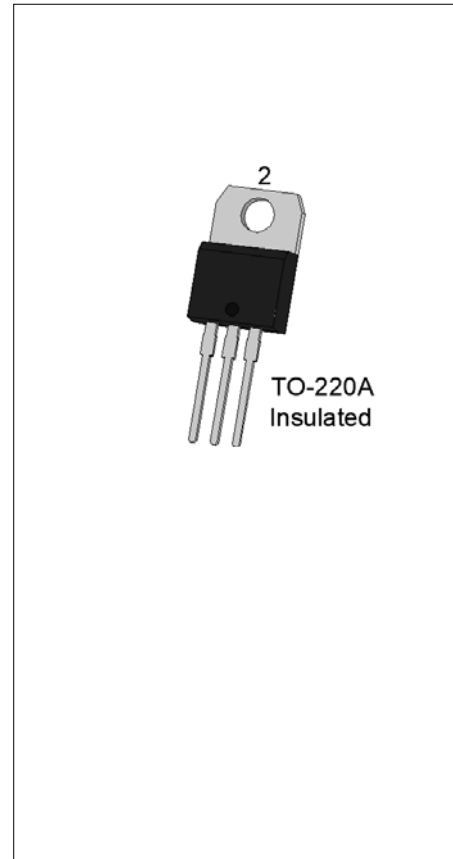




ACJT405-8A 4A TRIAC

Rev.A.1.0

The ACJT405-8A triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. The ACJT405-8A embeds a TVS structure to absorb the inductive turn-off energy such as those described in the IEC 61000-4-5 standards. By using an internal ceramic pad, ACJT405-8A provides a rated insulation voltage of 2500 VRMS, complying with UL standards (File ref: E252906). Package TO-220A is RoHS compliant.



Symbol	Value	Unit
$I_{T(RMS)}$	4	A
V_{DRM}/V_{RRM}	800	V
$I_{GT} / /$	5/5/5	mA

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{DRM}	800	V
Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	800	V
RMS on-state current ($T_c = 106^\circ\text{C}$)	$I_{T(RMS)}$	4	A
Non repetitive surge peak on-state current (full cycle, $t_p=20\text{ms}$, $T_j=25^\circ\text{C}$)	I_{TSM}	40	A
Non repetitive surge peak on-state current (full cycle, $t_p=16.6\text{ms}$, $T_j=25^\circ\text{C}$)		44	
I^2t value for fusing ($t_p=10\text{ms}$, $T_j=25^\circ\text{C}$)	I^2t	8	A^2s
Critical rate of rise of on-state current ($I_G=2 I_{GT}$, $f=100\text{Hz}$, $T_j=125^\circ\text{C}$)	di/dt	50	A/s
Peak gate current ($t_p=20\text{ }\mu\text{s}$, $T_j=125^\circ\text{C}$)	I_{GM}	4	A

Average gate power dissipation ($T_j=125$)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	10	W
Peak pulse voltage ($T_j=25$; non-repetitive,off-state;FIG.7)	V_{pp}	3	kV

(T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V R_L=33$	- -	MAX.	5	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	$V_D=V_{DRM} T_j=125$ $R_L=3.3K$	- -	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	-	MAX.	15	mA
				20	
I_H	$I_T=100mA$		MAX.	10	mA
dV/dt	$V_D=540V$ Gate Open $T_j=125$		MIN.	200	V s
(dI/dt) _c	(dV/dt) _c =1 $T_j=125$		MIN.	1.2	A/ms
t_{on}	$I_G=10mA I_A=200mA I_R=20mA$ $T_j=25$		TYP.	2	s
t_{off}				20	
V_{CL}	$I_{CL}=0.1mA t_p=1ms$		MIN.	850	V

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=5.6A t_p=380$ s	$T_j=25$	1.55	V
V_{TO}	Threshold voltage	$T_j=125$	0.73	V
R_D	Dynamic resistance	$T_j=125$	171	
I_{DRM}	$V_D=V_{DRM} V_R=V_{RRM}$	$T_j=25$	5	A
I_{RRM}		$T_j=125$	0.25	mA

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	3.3	/W
$R_{th(j-a)}$	junction to ambient (AC)	60	/W

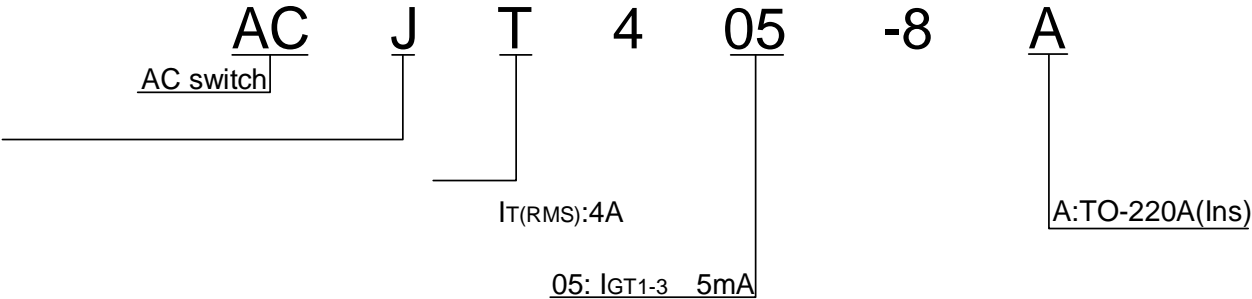


FIG.1 Maximum power dissipation versus RMS on-state current

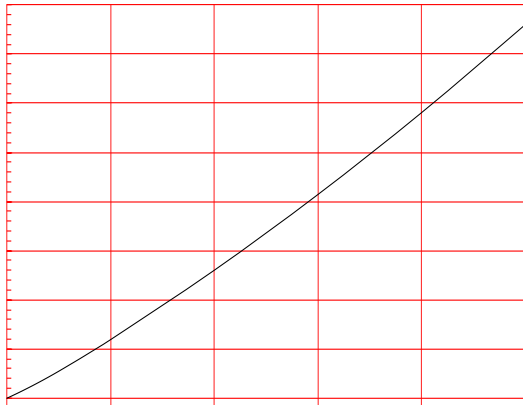


FIG.2: RMS on-state current versus case temperature

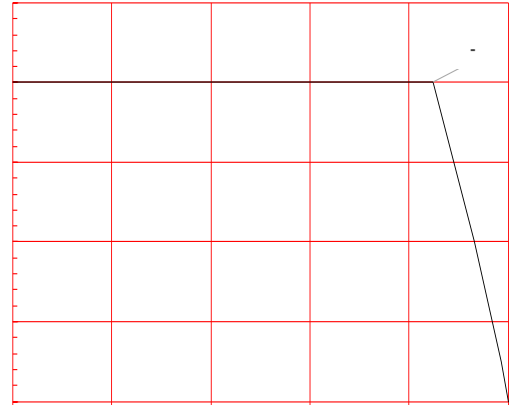


FIG.3: Surge peak on-state current versus number of cycles

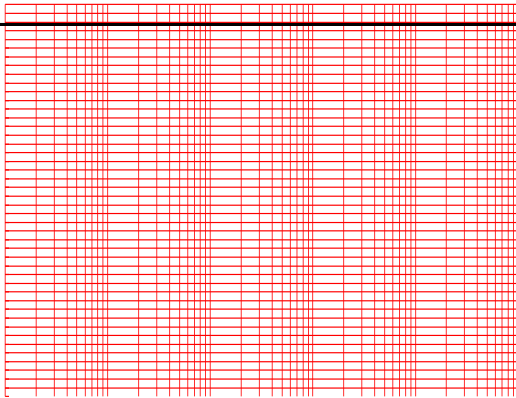
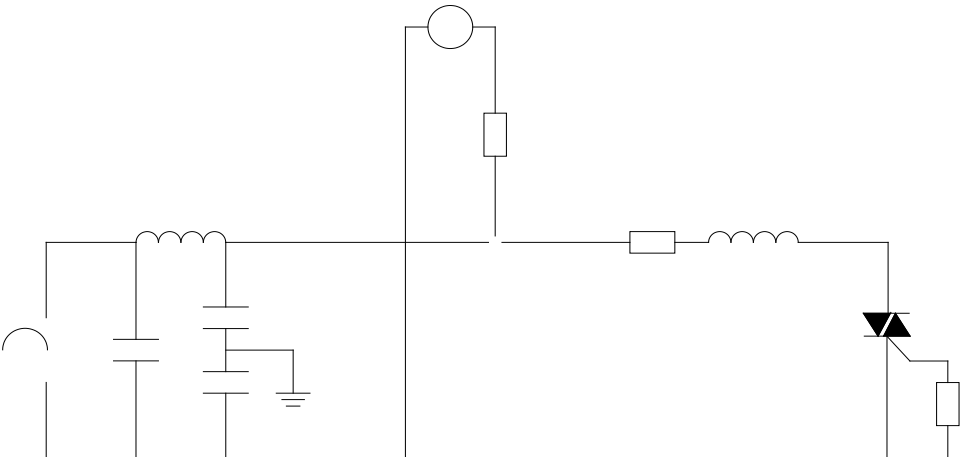


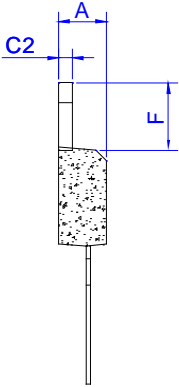
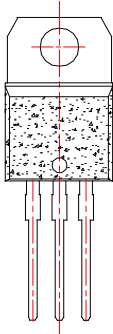
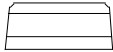
FIG.4: On-state characteristics

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
ACJT405-8A	800	5	TO-220A(Ins)	50	Tube

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