ACJT04A-1000SW 4A TRIAC

DESCRIPTION:

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T _{stg}	-40-150	
Operating junction temperature range	Tj	-40-125	
Repetitive peak off-state voltage ($T_j=25$)	V _{DRM}	1000	V
Repetitive peak reverse voltage ($T_j=25$)	Vrrm	1000	V
RMS on-state current (Tc 105)	I _{T(RMS)}	4	А
Non repetitive surge peak on-state current (full cycle , $t_p=20ms$, $T_j=25$)	I	40	۸
Non repetitive surge peak on-state current (full cycle , $t_p=16.6ms$, $T_j=25$)	ITSM	44	A

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JieJie Microelectronics CO. , Ltd.

Average gate power dissipation ($T_j=125$)	PG(AV)	0.5	W
Peak gate power	Рдм	10	W
Peak pulse voltage (Tj=25 ; non-repetitive,off-state;FIG.7)	V _{pp}	3.25	kV

ELECTRICAL CHARACTERISTICS (T_j=25 unless otherwise specified)

Symbol	Test Condition	Quadrant		Value	Unit
Ідт	- V _D =12V R∟=33		MAX.	10	mA
V _{GT}			MAX.	1	V

VGDj9262 / 8 0.4 /TT3 1 Tf244.2.472 13.()(=0 3V)2 0>BDC 0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td <078.01 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.4 6(1250 3V1 1 Tf 1.33 0 Td ()Tj EMC)Tj -0.007 Tc 0.58 6.5 700 Tc 0.58 6.5 700 Tc 0.58 700 Tc 0.58 700 TT 0.58 700 Tc 0.58 700 Tc 0.58 700 TC 0.58 700 TC



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FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



PACKAGE MECHANICAL DATA





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